

IMBA-Q77

Intel® 3rd Generation Core™ i3/i5/i7

Processor

DDR3 1066/1333 MHz DIMM

2 SATA 6.0Gb/s, 4 SATA 3.0Gb/s

1 PCI-Express[x16], 1 PCI-Express[x4]

2 PCI-Express[x1], 3 PCI

4 USB3.0, 8 USB2.0, 6 COM, 1 LPT

VGA, 1 DVI-D, 2 DisplayPort™

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Packing List

Before you begin installing your card, please make sure that the following materials have been shipped:

- 1 IMBA-Q77 ATX Main Board
- 2 SATA Cable
- 1 COM Port Cable
- 1 USB2.0 Cable
- 1 DVD-ROM for Manual (in PDF Format) and Drivers

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

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Chapter

1

**General
Information**

1.1 Introduction

The IMBA-Q77 supports Intel® 3rd generation Core™ i3/i5/i7 LGA1155 processor. Moreover it supports DDR3 1066/1333/1600MHz (1600 for 3rd generation Core i processors) memory up to 32GB. This model accommodates two Intel® Gigabit Ethernet controllers that those are controlled by Intel® 82579 (supports Intel® iAMT 8.0) and Intel® 82583V. This configuration provides outstanding computing ability, fast network connections and multi-task data transmission.

The graphic controller: Intel® HD Graphic supports three independent displays and the output interfaces equip onboard VGA, DVI-D x 1, and DisplayPort™ x 2 (HDMI optional) to meet the demand of the media and high definition. In addition, IMBA-Q77 deploys 8 USB2.0, 4 USB3.0, 6 COM, two PS/2 ports, and multiple extended bus for a flexible expansion selection. The storage of IMBA-Q77 supports four SATA 3.0 Gb/s and two SATA 6.0 Gb/s to support RAID 0, 1, 5, 10 functions.

The IMBA-Q77 provides an ideal combination of high performance, widely expandable interfaces and compact size that is easy to apply for multiple applications. The IMBA-Q77 will be an ideal product for your requirement.

1.2 Features

- Intel® 3rd Generation Core™ i7/ i5/ i3 LGA 1155 Processor
- Intel® Q77
- Dual-Channel DDR3 1066/1333/1600 DIMM (1600 for 3rd Generation Core™ i Processors) x 4, Up to 32 GB
- 10/100/1000Base-TX x 2 (LAN1 Supports Intel® iAMT 8.0)
- Three Independent Displays For 3rd Generation Core™ i Processors With VGA, DisplayPort™, DVI-D
- SATA 3.0Gb/s x 4, SATA 6.0Gb/s x 2, Support RAID 0,1,5,10
- USB2.0 x 8, USB3.0 x 4 , COM x 6, LPT x 1, IrDA Tx/Rx Header x 1
- PCI-Express[x16] x 1, PCI-Express[x4] x 1, PCI-Express[x1] x 2, PCI x 3
- TPM 1.2 (Optional)

1.3 Specifications

System

- Form Factor ATX
- Processor Intel® 3rd generation Core™ i3/i5/i7 LGA 1155 Processor
- System Memory Dual Channel DDR3
1066/1333/1600MHz DIMM (1600 for 3rd Generation Core™ i Processors) x 4, up to 32 GB, Unbuffered memory
- Chipset Intel® Q77
- Ethernet Gigabit Ethernet, RJ-45 x 2
LAN1: Intel® 82579 (supports Intel® iAMT 8.0);
LAN2: Intel® 82583V
- BIOS AMI SPI Flash ROM-128Mb ROM
- Watchdog Timer System reset: 1~255 steps by software programming
- H/W Status Monitoring System temperature, voltage and cooling fan status
- Battery Lithium battery
- Expansion Interface PCI-Express[x16] x 1,
PCI-Express[x4 x 1,
PCI-Express[x1] x 2, PCI x 3, TPM

- Power Requirement 1.2 onboard (optional)
ATX standard 24-pin connector x 1, 4-pin +12V connector x 1, CPU fan x 1, system fan x 1 with 4-pin wafer, supports SMART FAN control
- Operating Temperature 32°F ~140°F (0°C ~60°C)
- Storage Temperature -4°F ~158°F (-20°C ~70°C)
- Storage Humidity 5%~90%, non-condensing
- Board Size (L x W) 12" x 9.6" (305 x 244 mm)
- Gross Weight 1.76(0.8 Kg)
- EMC CE & FCC Class A

Display

- Chipset Intel® Core™ i3/i5/i7 + Q77
- Graphic Engine Intel® HD Graphic support, three independent display for 3rd generation Core™ i Processors
- Resolution Up to 2048x1536 @ 75Hz for CRT; Up to 2560x1600 @ 85Hz for DisplayPort™, 1080P for HDMI (Optional)
- Output Interface Onboard VGA x 1, DVI-D x 1, DisplayPort™ x 2 (HDMI optional)

I/O: Winbond W83627DHG-P + Fintek F81216AD

- Storage SATA 3.0 Gb/s x 4, SATA 6.0 Gb/s x 2, support RAID 0,1,5,10
- Serial Port COM x 6 (box header x 5, external DB-9 x 1),
COM1: RS-232/422/485 (external DB-9)
COM2~6: RS-232 (box header)
- Keyboard & Mouse Keyboard x 1, Mouse x 1
- Universal Serial Bus USB2.0 x 8, USB3.0 x 4
- Audio Audio Jack x 3 (Mic-in, Line-in, Line-out)
- Digital I/O 8-bit programmable (4-in/ 4-out)
- IrDA Supports one IrDA header (supports Windows XP only)

Chapter

2

**Quick
Installation
Guide**

2.1 Safety Precautions

Warning!

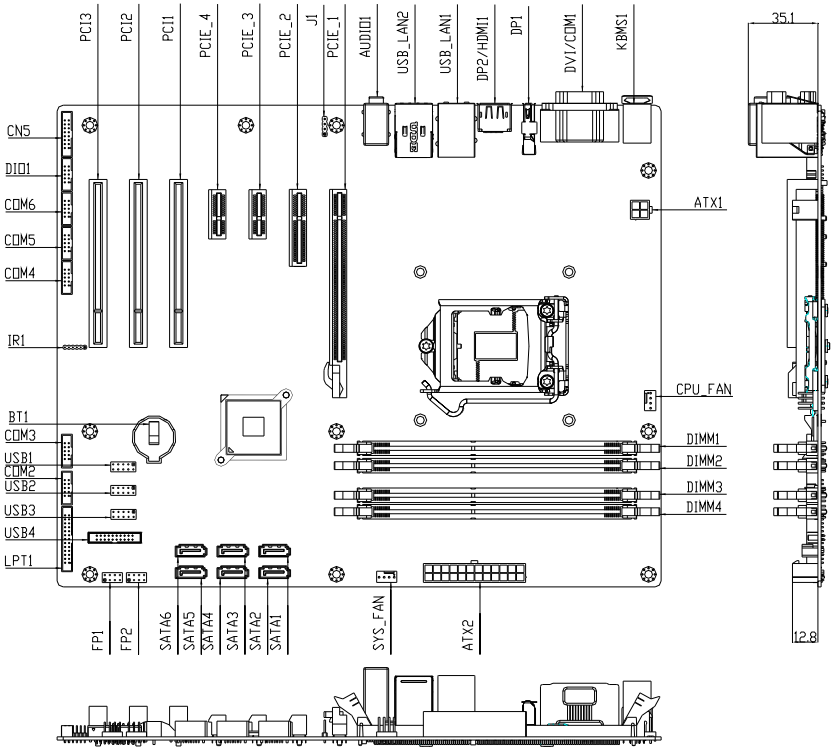
Always completely disconnect the power cord from your board whenever you are working on it. Do not make connections while the power is on, because a sudden rush of power can damage sensitive electronic components.

Caution!

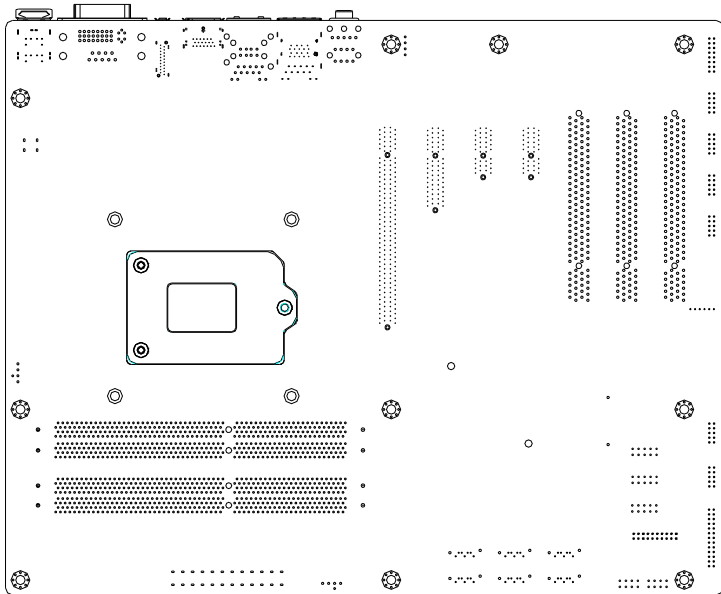
Always ground yourself to remove any static charge before touching the board. Modern electronic devices are very sensitive to static electric charges. Use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis

2.2 Location of Connectors and Jumpers

Component Side

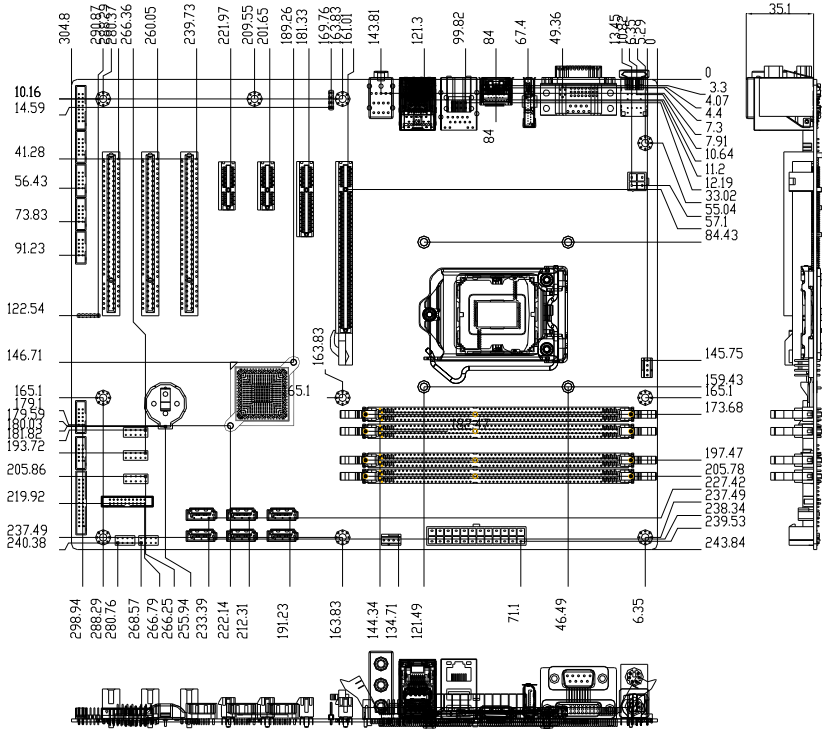


Solder Side

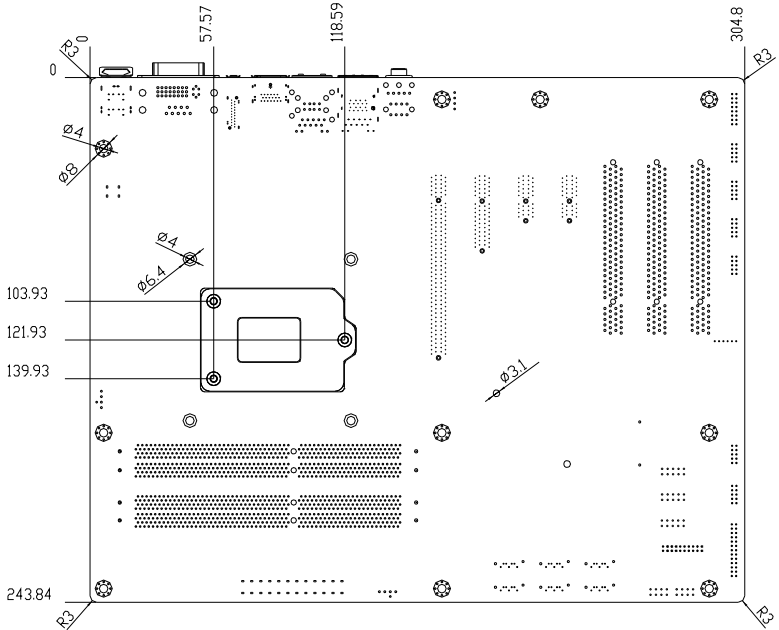


2.3 Mechanical Drawing

Component Side



Solder Side



2.4 List of Jumpers

The board has a number of jumpers that allow you to configure your system to suit your application.

The table below shows the function of each of the board's jumpers:

Label	Function
JP1	Clear CMOS
JP3	AUTO POWER BUTTOM

2.5 List of Connectors

The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each board's connectors:

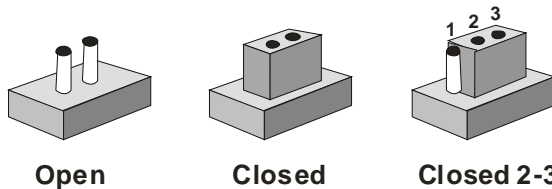
Label	Function
FP1	Front Panel Connector 1
FP2	Front Panel Connector 2
CN5	VGA Port Pin Header
COM2	RS-232 Pin Header
COM3	RS-232 Pin Header
COM4	RS-232 Pin Header
COM5	RS-232 Pin Header
COM6	RS-232 Pin Header
DIO1	Digital I/O Pin Header
LPT1	Parallel Port Pin Header
USB1	USB Pin Header
USB2	USB Pin Header
USB3	USB Pin Header
USB4	USB 3.0 Pin Header
BT1	Battery
IR1	IR Pin Header
SATA1~SATA6	SATA Connector
USB_LAN1	USB & 10/100/1000Base-T Ethernet Connector
USB_LAN2	USB3.0 & 10/100/1000Base-T Ethernet Connector
DIMM1	DDR3 DIMM Slot

DIMM2	DDR3 DIMM Slot
DIMM3	DDR3 DIMM Slot
DIMM4	DDR3 DIMM Slot
AUDIO1	AUDIO Connector
CPU_FAN1	4-Pin Fan Connector
CHASSIS_FAN1	4-Pin Fan Connector
SYS_FAN1	4-Pin Fan Connector
CN2	DVI-D / COM1 RS232/422/485
DP1	Display Port1
DP2/HDMI1	Display Port2 / HDMI
KBMS1	PS/2 KB / MS
ATX1	4 PIN ATX 12V
ATX2	ATX Connector
PCIE_1	PCI-E [x16] Connector
PCIE_2	PCI-E [x4] Connector
PCIE_3	PCI-E [x1] Connector
PCIE_4	PCI-E [x1] Connector
PCI1	PCI Connector
PCI2	PCI Connector
PCI3	PCI Connector

2.6 Setting Jumpers

You configure your card to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip.

To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2 and 3. In this case you would connect either pins 1 and 2 or 2 and 3.



A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any change.

Generally, you simply need a standard cable to make most connections.

2.7 Clear CMOS (JP1)

JP1	Function
1-2	Protected (Default)
2-3	Clear

2.8 Auto Power Button(JP3)

JP3	Function
1-2	Power ON by Button (Default)
2-3	Auto Power ON

2.9 Front Panel Connector (FP1)

Pin	Signal	Pin	Signal
1	Power On Button (+)	2	Reset Switch (+)
3	Power On Button (-)	4	Reset Switch (-)
5	HDD LED (+)	6	Power LED (+)
7	HDD LED (-)	8	Power LED (-)

2.10 Front Panel Connector (FP2)

Pin	Signal	Pin	Signal
1	External Speaker (+)	2	Key Board Lock (+)
3	NC	4	GND
5	Internal Buzzer (-)	6	I2C Bus SMB Clock
7	External Speaker (-)	8	I2C Bus SMB Data

Note: Internal Buzzer Enable: Close Pin 5,7

2.11 RS-232 Serial Port Connector (COM2, 3, 4, 5, 6)

Pin	Signal	Pin	Signal
1	DCD	2	RXD
3	TXD	4	DTR
5	GND	6	DSR
7	RTS	8	CTS
9	RI		

2.12 IR Pin Header (IR1)

Pin	Signal
1	+5V
2	NC
3	RX
4	GND
5	TX

2.13 Digital I/O Pin Header (DIO1)

Pin	Signal	Pin	Signal
1	DIO_30	2	DIO_31
3	DIO_32	4	DIO_33
5	DIO_34	6	DIO_35
7	DIO_36	8	DIO_37
9	+3.3V	10	GND

2.14 VGA Port PIN Header (CN5)

Pin	Signal	Pin	Signal
1	VGA_RED_C	2	V_VDO_5V
3	VGA_GRE_C	4	GND
5	VGA_BLE_C	6	NC
7	NC	8	VDO_MONID1_R
9	GND	10	V_HSYNC
11	GND	12	V_VSYNC
13	GND	14	VDO_MONID2_R
15	GND	16	NC

2.15 USB3.0 Port PIN Header (USB4)

Pin	Signal	Pin	Signal
1	VCC	20	NC
2	USB3_RX1_DN_C	19	VCC
3	USB3_RX1_DP_C	18	USB3_RX2_DN_C
4	GND	17	USB3_RX2_DP_C
5	USB3_TX1_DN_C	16	GND
6	USB3_TX1_DP_C	15	USB3_TX2_DN_C
7	GND	14	USB3_TX2_DP_C
8	USBP_0N_C	13	GND
9	USBP_0P_C	12	USBP_1N_C
10	NC	11	USBP_1P_C

Below Table for China RoHS Requirements

产品中有毒有害物质或元素名称及含量

AAEON Main Board/ Daughter Board/ Backplane

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
印刷电路板 及其电子组件	×	○	○	○	○	○
外部信号 连接器及线材	×	○	○	○	○	○
<p>O: 表示该有毒有害物质在该部件所有均质材料中的含量均在 SJ/T 11363-2006 标准规定的限量要求以下。</p> <p>X: 表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 SJ/T 11363-2006 标准规定的限量要求。</p> <p>备注: 此产品所标示之环保使用期限, 系指在一般正常使用状况下。</p>						

Chapter

3

**AMI
BIOS Setup**

3.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors.

System configuration verification

These routines check the current system configuration stored in the CMOS memory and BIOS NVRAM. If system configuration is not found or system configuration data error is detected, system will load optimized default and re-boot with this default system configuration automatically.

There are four situations in which you will need to setup system configuration:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The system configuration is reset by Clear-CMOS jumper
4. The CMOS memory has lost power and the configuration information has been erased.

The IMBA-Q77 CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

3.2 AMI BIOS Setup

AMI BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM and BIOS NVRAM so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer and press or <F2> immediately. This will allow you to enter Setup.

Main

Set the date, use tab to switch between date elements.

Advanced

Enable/disable boot option for legacy network devices.

Chipset

Host bridge parameters.

Boot

Enables/disables quiet boot option.

Security

Set setup administrator password.

Save & Exit

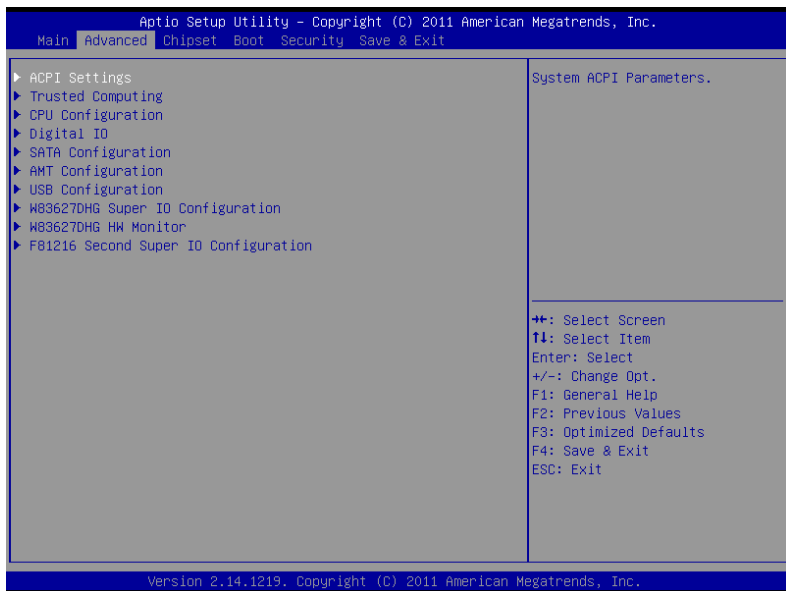
Exit system setup after saving the changes.

Setup Menu

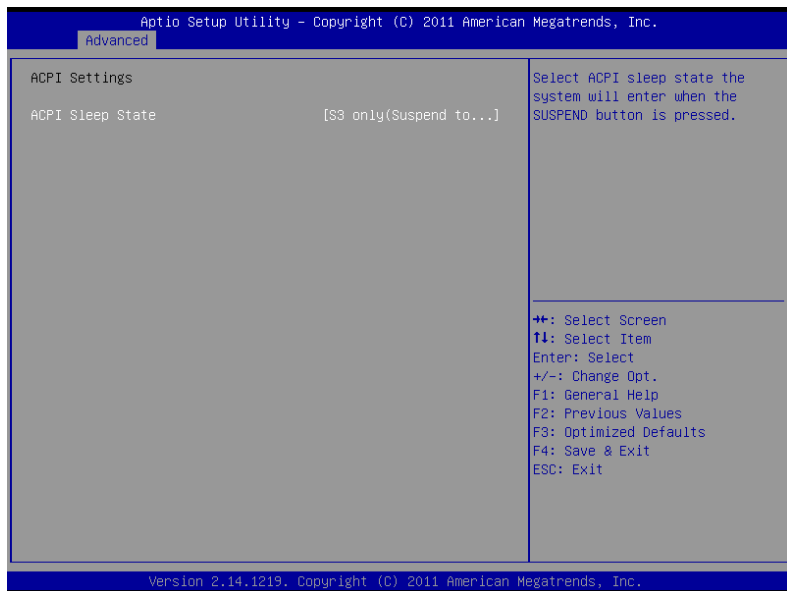
Setup submenu: Main

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Main Advanced Chipset Boot Security Save & Exit		
BIOS Information IMBA-Q77 R1.6(IQ77AM16) (05/01/2012)		Set the Date. Use Tab to switch between Date elements.
BIOS Vendor Core Version Compliancy Project Version Build Date and Time	American Megatrends 4.6.5.3 x64 UEFI 2.3; PI 1.2 IQ77AM16 05/08/2012 14:00:30	
System Date System Time	[Fri 02/27/2009] [09:48:24]	
Access Level	Administrator	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219, Copyright (C) 2011 American Megatrends, Inc.		

Setup submenu: Advanced



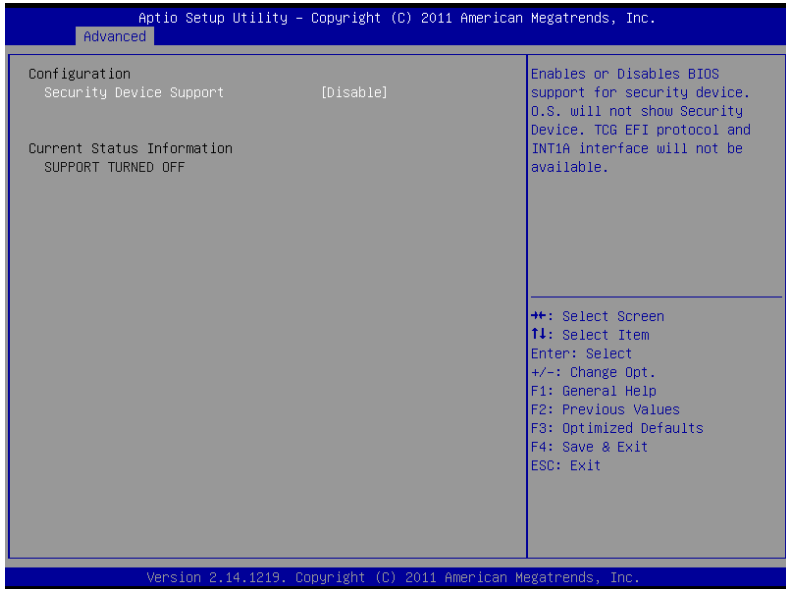
ACPI Settings



Options summary:

Suspend mode	S1 only (CPU Stop Clock)	Optimal Default, Failsafe Default
	S3 only (Suspend to RAM)	
Select the ACPI state used for System Suspend		

Trusted Computing



Options summary:

Security Device Support	Disable	Optimal Default, Failsafe Default
	Enable	
Enable or Disables BIOS support for security device. O.S. will not show Security Device. TCG EFI protocol and INT1A interface will not be available.		

CPU Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.

Advanced

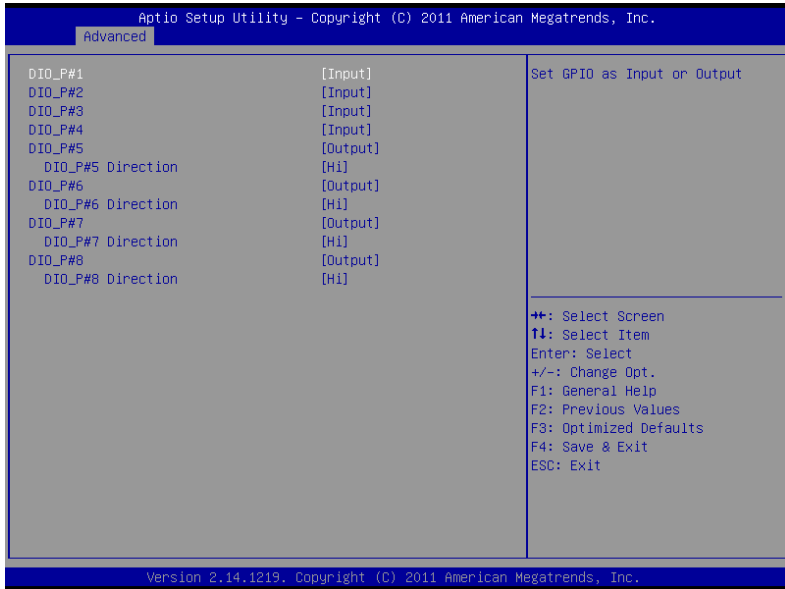
CPU Configuration		Enabled for Windows XP and Linux (OS optimized for Hyper-Threading Technology) and Disabled for other OS (OS not optimized for Hyper-Threading Technology). When Disabled only one thread per enabled core is enabled.
Intel(R) Core(TM) i7-2600 CPU @ 3.40GHz		
CPU Signature	206a7	
Microcode Patch	25	
Max CPU Speed	3400 MHz	
Min CPU Speed	1600 MHz	
CPU Speed	3400 MHz	
Processor Cores	4	
Intel HT Technology	Supported	
Intel VT-x Technology	Supported	
Intel SMX Technology	Supported	
64-bit	Supported	
L1 Data Cache	32 kB x 4	
L1 Code Cache	32 kB x 4	
L2 Cache	256 kB x 4	
L3 Cache	8192 kB	
Hyper-threading	[Enabled]	
Intel Virtualization Technology	[Disabled]	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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Options summary:

Hyper-Threading	Disabled	Optimal Default, Failsafe Default
	Enabled	
En/Disable CPU Hyper-Threading function		
Intel Virtualization Technology	Disabled	Optimal Default, Failsafe Default
	Enabled	
When enabled, a VMM can utilize the additional hardware capabilities provided by Vanderpool Technology		

Digital IO



Options summary:

DIO_P#1~4	Input	Default
	Output	
Allows BIOS to select input/output function to corresponding DIO ping.		
DIO_P#5~8	Input	Default
	Output	
Allows BIOS to select input/output function to corresponding DIO ping.		
DIO_P#5~8 Direction	Low	Default
	Hi	
Allows BIOS to select high/low voltage level to output to corresponding DIO ping.		

IDE Configuration (IDE)

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Advanced		
SATA Controller(s)	[Enabled]	Enable or disable SATA Device.
SATA Mode Selection	[IDE]	
Serial ATA Port 0	Empty	
Software Preserve	Unknown	
Serial ATA Port 1	Empty	
Software Preserve	Unknown	
Serial ATA Port 2	Empty	
Software Preserve	Unknown	
Serial ATA Port 3	Empty	
Software Preserve	Unknown	
Serial ATA Port 4	Empty	
Software Preserve	Unknown	
Serial ATA Port 5	Empty	
Software Preserve	Unknown	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219, Copyright (C) 2011 American Megatrends, Inc.		

IDE Configuration (AHCI)

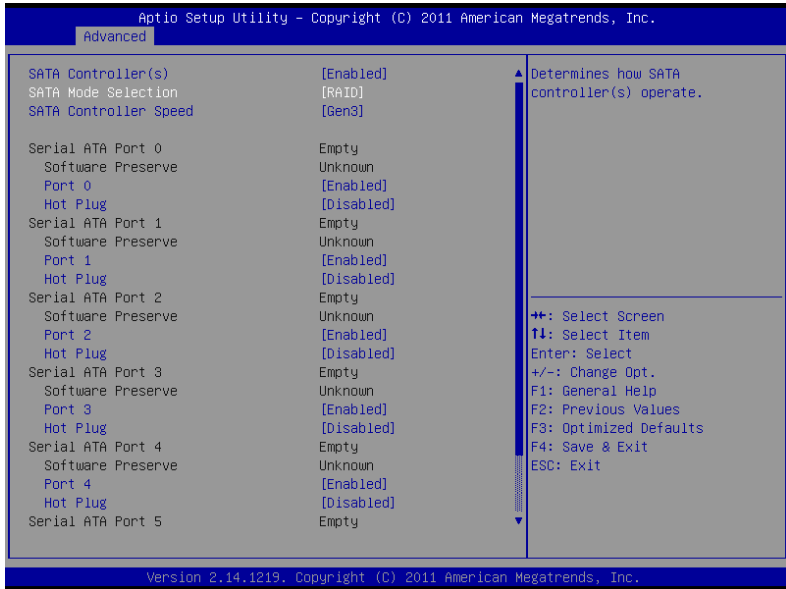
Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.

Advanced

SATA Controller(s)	[Enabled]	▲ Determines how SATA controller(s) operate.
SATA Mode Selection	[AHCI]	
SATA Controller Speed	[Gen3]	
Serial ATA Port 0	Empty	++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Software Preserve	Unknown	
Port 0	[Enabled]	
Hot Plug	[Disabled]	
Serial ATA Port 1	Empty	
Software Preserve	Unknown	
Port 1	[Enabled]	
Hot Plug	[Disabled]	
Serial ATA Port 2	Empty	
Software Preserve	Unknown	
Port 2	[Enabled]	
Hot Plug	[Disabled]	
Serial ATA Port 3	Empty	
Software Preserve	Unknown	
Port 3	[Enabled]	
Hot Plug	[Disabled]	
Serial ATA Port 4	Empty	
Software Preserve	Unknown	
Port 4	[Enabled]	
Hot Plug	[Disabled]	
Serial ATA Port 5	Empty	▼

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IDE Configuration (RAID)



Options summary:

SATA Controllers	Disabled	Default
	Enabled	
En/Disable SATA Controller.		
SATA Mode	IDE	Default
	AHCI	
	RAID	
IDE: Configure SATA controllers as legacy IDEAHCI: Configure SATA controllers to operate in AHCI mode		
SATA Controller Speed	Gen1	Default
	Gen2	
	Gen3	
Indicates the maximum speed the SATA controller can support.		
Port x	Disabled	Optimal Default, Failsafe Default
	Enabled	
En/Disable SATA Port.		
Hot Plug	Disabled	Optimal Default, Failsafe Default
	Enabled	
En/Disable Hot Plug feature.		

AMT Configuration

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Advanced

Intel AMT	[Enabled]	Enable/Disable Intel (R) Active Management Technology BIOS Extension. Note : iAMT H/W is always enabled. This option just controls the BIOS extension execution. If enabled, this requires additional firmware in the SPI device
Un-Configure ME	[Disabled]	

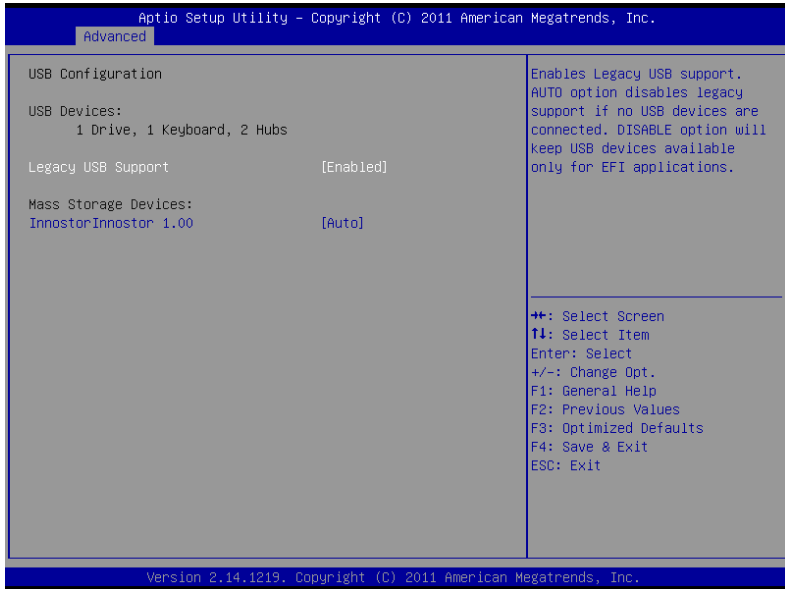
++: Select Screen
 ↑↓: Select Item
 Enter: Select
 +/-: Change Opt.
 F1: General Help
 F2: Previous Values
 F3: Optimized Defaults
 F4: Save & Exit
 ESC: Exit

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Options summary:

Intel AMT	Disabled	Optimal Default, Failsafe Default
	Enabled	
En/Disable Intel Active Management Technology BIOS Extension. Note: iAMT H/W is always enabled. This option just controls the BIOS extension execution. If enabled, this requires additional firmware in the SPI device.		
Un-configure ME	Disabled	Optimal Default, Failsafe Default
	Enabled	
OEMFlag Bit 15: Un-Configure ME without password.		

USB Configuration



Options summary:

Legacy USB Support	Enabled	Optimal Default, Failsafe Default
	Disabled	
	Auto	
Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected		
Device Name (Emulation Type)	Auto	Optimal Default, Failsafe Default
	Floppy	
	Forced FDD	
	Hard Disk	
	CDROM	
If Auto. USB devices less than 530MB will be emulated as Floppy and remaining as Floppy and remaining as hard drive. Forced FDD option can be used to force a HDD formatted drive to boot as FDD(Ex. ZIP drive)		

W83627DHG Super IO Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.

Advanced

W83627DHG Super IO Configuration		Set Parameters of Serial Port 1 (COMA)
W83627DHG Super IO Chip	W83627DHG	
▶ Serial Port 1 Configuration		
▶ Serial Port 2 Configuration		
▶ Parallel Port Configuration		
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.

Serial Port 1 Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Advanced		
Serial Port 1 Configuration		Enable or Disable Serial Port (COM)
Serial Port	[Enabled]	
Device Settings	IO=3F8h; IRQ=4;	
Device Mode	[RS232]	
Change Settings	[Auto]	
		++: Select Screen ↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.		

Options summary:

Serial Port	Disabled	Default
	Enabled	
Allows BIOS to En/Disable correspond serial port.		
Device Mode	RS232	Default
	RS422	
	RS485	
Select working model.		
Change Settings	Auto	Default
	IO=3F8h; IRQ=4;	
	IO=3F8h; IRQ=3,4;	
	IO=2F8h; IRQ=3,4;	
	IO=3E8h; IRQ=3,4;	
	IO=2E8h; IRQ=3,4;	
Allows BIOS to Select Serial Port resource.		

Serial Port 2 Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Advanced		
Serial Port 2 Configuration		Enable or Disable Serial Port (COM)
Serial Port	[Enabled]	
Device Settings	IO=2F8h; IRQ=3;	
Change Settings	[Auto]	
Device Mode	[Standard Serial Po...]	
		++: Select Screen ↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.		

Options summary:

Serial Port	Disabled	Default
	Enabled	
Allows BIOS to En/Disable correspond serial port.		
Change Settings	Auto	Default
	IO=2F8h; IRQ=3;	
	IO=3F8h; IRQ=3,4;	
	IO=2F8h; IRQ=3,4;	
	IO=3E8h; IRQ=3,4;	
IO=2E8h; IRQ=3,4;		
Allows BIOS to Select Serial Port resource.		
Device Mode	Standard Serial Port Mode	Default
	IrDA Active pulse 1.6 uS	
	IrDA Active pulse 3/16 bit time	
	ASK-IR Inverting IRTX&500KHZ, Demodulation to IRRX	
Change the Serial Port mode. Select <High Speed> or <Normal mode> mode.		

Parallel Port Configuration

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Advanced

Parallel Port Configuration		Enable or Disable Parallel Port (LPT/LPTE)
Parallel Port	[Enabled]	
Device Settings	IO=378h; IRQ=5;	
Change Settings	[Auto]	
Device Mode	[STD Printer Mode]	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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Options summary:

Parallel Port	Disabled	Default
	Enabled	
Allows BIOS to En/Disable Parallel port.		
Change Settings (Parallel Port)	Auto	Default
	IO=378h; IRQ=5;	
	IO=378h; IRQ=5,7;	
	IO=278h; IRQ=5,7;	
	IO=3BCh; IRQ=5,7;	
Allows BIOS to Select Serial Port resource.		
Device Mode	STD Printer Mode	Default
	SPP Mode	
	EPP-1.9 and SPP Mode	
	EPP-1.9 and SPP Mode	
	ECP Mode	
	ECP and EPP 1.9 Mode	
	ECP and EPP 1.7 Mode	
Change the Printer Port mode.		

W83627DHG H/W Monitor

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Advanced

Pc Health Status		Enable or Disable Smart Fan
Smart Fan Function	[Enabled]	
▶ Smart Fan Mode Configuration		
SYSTIN temperature	: +29 ℃	
CPUTIN temperature	: +49 ℃	
system Fan Speed	: N/A	
CPU Fan0 Speed	: 1028 RPM	
AUX Fan0 Speed	: N/A	
VCORE	: +1.160 V	
VIN0 +12V	: +12.160 V	
VIN1 +3.3V	: +3.408 V	
VIN2 V_SM	: +1.520 V	
AVCC	: +3.424 V	
VCC3V	: +3.424 V	
VSB3	: +3.440 V	
VBAT	: N/A	
		→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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Options summary:

Smart Fan Function	Disabled	Default
	Enabled	
Allows BIOS to En/Disable Smart Fan.		

Smart Fan Mode Configuration (Manual Mode)

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.	
Advanced	
Smart Fan Mode Configuration	SYS Smart Fan Mode Select
SYS Smart Fan Mode [Manual Mode]	
SYSFAN expect PWM Output/DC Voltag 128	
CPU Smart Fan 0 Mode [Manual Mode]	
CPUFAN0 expect PWM Output/DC Volta 128	
AUX Smart Fan Mode [Manual Mode]	
AUXFAN expect PWM Output/DC Voltag 128	
	→+: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.	

Smart Fan Mode Configuration (Thermal Cruise Mode)

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.	
Advanced	
Smart Fan Mode Configuration	SVS Smart Fan Mode Select
SYS Smart Fan Mode	[Thermal Cruise Mode]
SYSFAN Target Temperature	50
SYSFAN Tolerance of Target Tempera	5
CPU Smart Fan 0 Mode	[Thermal Cruise Mode]
CPUFAN0 Target Temperature	50
CPUFAN0 Tolerance of Target Temper	5
AUX Smart Fan Mode	[Thermal Cruise Mode]
AUXFAN Target Temperature	50
AUXFAN Tolerance of Target Tempera	5
	++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
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Smart Fan Mode Configuration (Fan Speed Cruise Mode)

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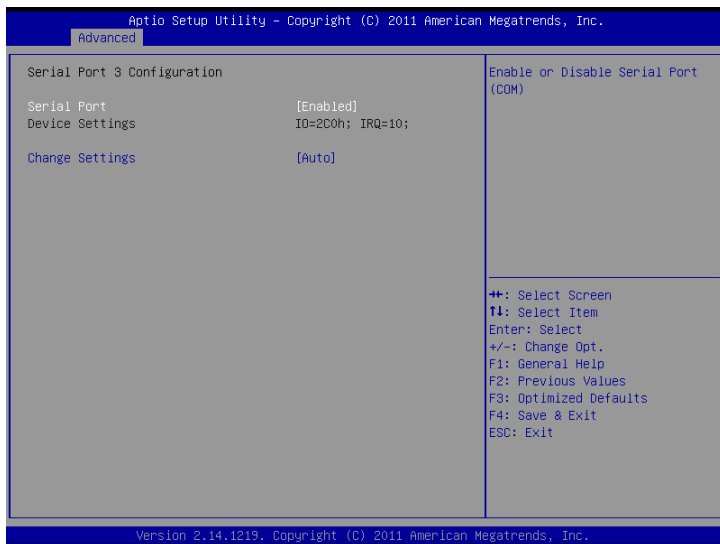
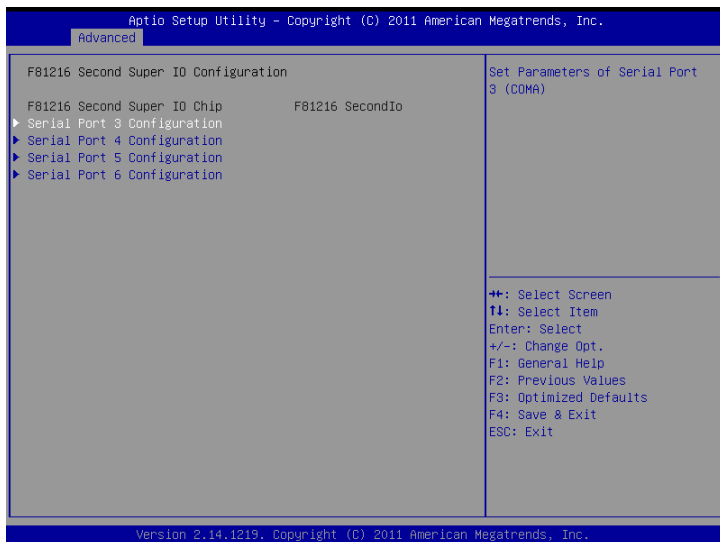
Advanced

Smart Fan Mode Configuration	SYS Smart Fan Mode Select
SYS Smart Fan Mode [Fan Speed Cruise Mode]	
SYSFAN Target Speed Count 10000	
SYSFAN Tolerance of Target Speed C 10	
CPU Smart Fan 0 Mode [Fan Speed Cruise Mode]	
CPUFAN0 Target Speed Count 10000	
CPUFAN0 Tolerance of Target Speed 10	
AUX Smart Fan Mode [Fan Speed Cruise Mode]	
AUXFAN Target Speed Count 10000	
AUXFAN Tolerance of Target Speed C 10	
	++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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Options summary:

SYS Smart Fan Mode CPU Smart Fan 0 Mode AUX Smart Fan Mode	Manual Mode	Default
	Thermal Cruise Mode Fan Speed Cruise Mode	
Smart Fan Mode Select.		
(SYSFAN/CPUFAN0/AUXFAN) expect PWM Output/DC Voltage	0 ~ 255	Default (128)
Input expect PWM Output Value (Range: 0 - 255)		
(SYSFAN/CPUFAN0/AUXFAN) Target Temperature	0 ~ 127	Default (50)
Input Target Temperature (Range: 0 - 127)		
(SYSFAN/CPUFAN0/AUXFAN) Tolerance of Target Temperature	0 ~ 15	Default (5)
Input Tolerance of Target Temperature (Range: 0 - 15)		
(SYSFAN/CPUFAN0/AUXFAN) Target Speed Count	0 ~ 20000	Default (10000)
Input Target Speed Count (Range: 0 - 20000)		
(SYSFAN/CPUFAN0/AUXFAN) Tolerance of Target Speed Count	0 ~ 15	Default (10)
Input Tolerance of Target Speed Count (Range: 0 - 15)		



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Advanced

Serial Port 4 Configuration		Enable or Disable Serial Port (CDM)
Serial Port	[Enabled]	
Device Settings	IO=2C0h; IRQ=10;	
Change Settings	[Auto]	
		++: Select Screen ↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

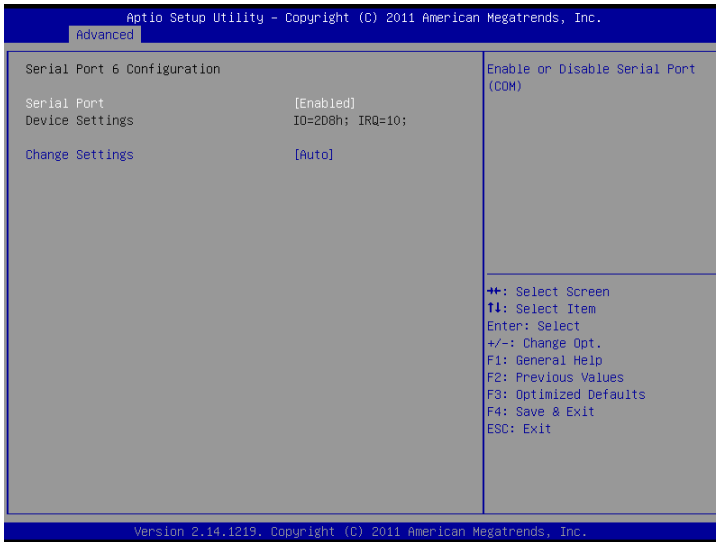
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Advanced

Serial Port 5 Configuration		Enable or Disable Serial Port (CDM)
Serial Port	[Enabled]	
Device Settings	IO=2D0h; IRQ=10;	
Change Settings	[Auto]	
		++: Select Screen ↑: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

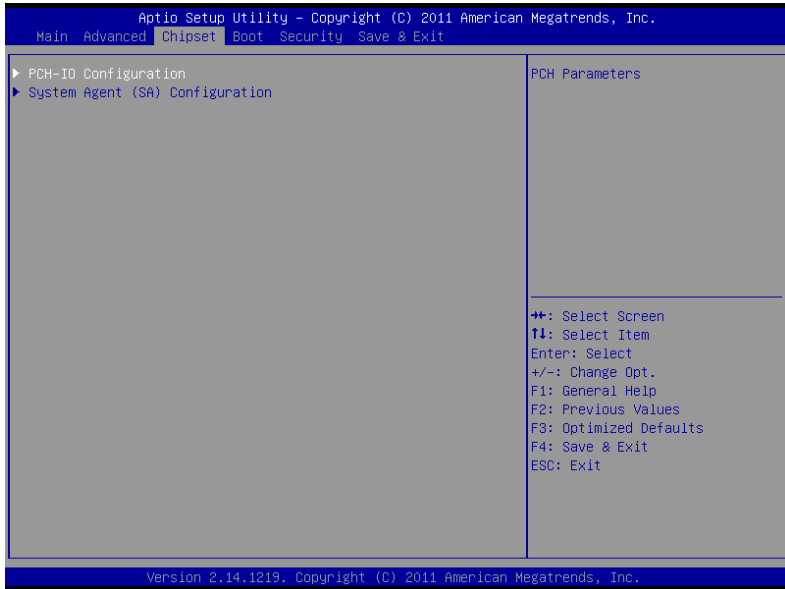
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Options summary:

Serial Port	Disabled	Default
	Enabled	
Allows BIOS to En/Disable correspond serial port.		
Change Settings (Serial Port 3)	Auto	Default
	IO=2C0h; IRQ=10;	
	IO=2C0h; IRQ=10,11; IO=2C8h; IRQ=10,11;	
Change Settings (Serial Port 4)	Auto	Default
	IO=2C8h; IRQ=10;	
	IO=2C0h; IRQ=10,11; IO=2C8h; IRQ=10,11;	
Change Settings (Serial Port 5)	Auto	Default
	IO=2D0h; IRQ=10;	
	IO=2D0h; IRQ=10,11; IO=2D8h; IRQ=10,11;	
Change Settings (Serial Port 6)	Auto	Default
	IO=2D8h; IRQ=10;	
	IO=2D0h; IRQ=10,11; IO=2D8h; IRQ=10,11;	
Allows BIOS to Select Serial Port resource.		

Setup submenu: Chipset



PCH-IO Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Chipset		
▶ PCH Azalia Configuration		PCH Azalia Configuration settings.
82579LM LAN Controller	[Enabled]	
82583V LAN Controller	[Enabled]	
PCIE_2 Slot (x4) Speed	[Auto]	
PCIE_3 Slot (x1) Speed	[Auto]	
PCIE_4 Slot (x1) Speed	[Auto]	
Power Mode	[ATX Type]	
Restore AC Power Loss	[Last State]	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.		

Options summary:

82579LM LAN Controller	Disabled	Default
	Enabled	
Enable or disable onboard NIC.		
82583V LAN Controller	Disabled	Default
	Enabled	
Control the PCI Express Root Port		
PCIE_2 Slot (x4) Speed	Auto	Default
PCIE_3 Slot (x1) Speed	Gen1	
PCIE_4 Slot (x1) Speed	Gen2	
Select PCI Express port speed.		
Power Mode	ATX Type	Default
	AT Type	
Select power supply mode.		
Restore AC Power Loss	Always OFF	Default
	Always ON	
	Last State	
Select AC power state when power is re-applied after a power failure.		

PCH Azalia Configuration

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Chipset

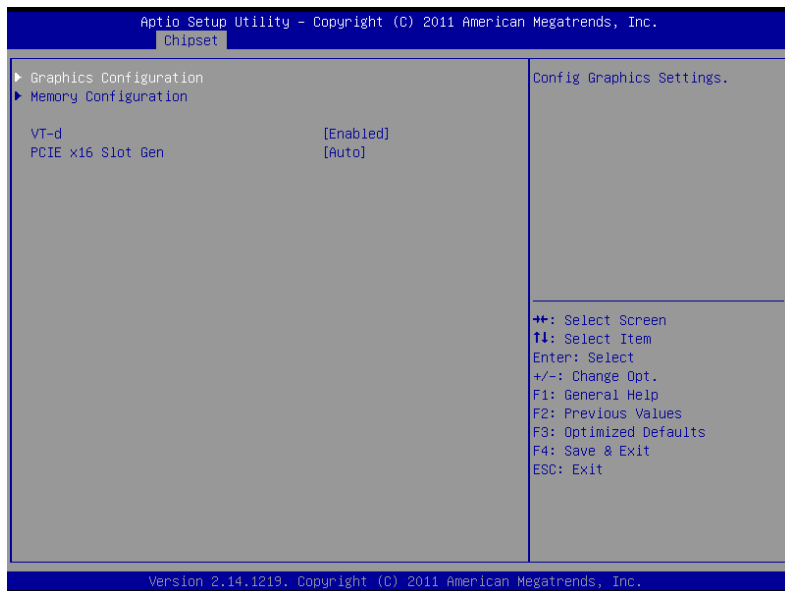
PCH Azalia Configuration		Control Detection of the Azalia device. Disabled = Azalia will be unconditionally disabled Enabled = Azalia will be unconditionally Enabled Auto = Azalia will be enabled if present, disabled otherwise.
Azalia	[Auto]	
Azalia Internal HDMI Codec	[Enabled]	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit

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Options summary:

Azalia	Disabled	Default
	Enabled	
	Auto	
Control Detection of the Azalia device. Disabled = Azalia will be unconditionally disabled; Enabled = Azalia will be unconditionally enabled; Auto = Azalia will be enabled if present, disabled otherwise.		
Azalia Internal HDMI Codec	Disabled	Default
	Enabled	
Enable or disable internal HDMI codec for Azalia.		

System Agent (SA) Configuration



Options summary:

VT-d	Disabled	Default
	Enabled	
Check to enable VT-d function on MCH		
PCIe x16 Slot Gen	Auto	Default
	Gen1	
	Gen2	
	Gen3	
Configure PEG0 B0D1:F0 Gen1-Gen3		

Graphics Configuration

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Chipset		
Graphics Configuration		Select which of IGFX/PEG/PCI Graphics device should be Primary Display Or select SG for Switchable Gfx.
Primary Display	[Auto]	
Internal Graphics	[Auto]	
GTT Size	[2MB]	
Aperture Size	[256MB]	
DVMT Pre-Allocated	[64M]	
DVMT Total Gfx Mem	[256M]	
Primary IGFX Boot Display	[VBIOS Default]	
		++: Select Screen ↑↓: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.		

Options summary:

Primary Display	Auto	Default
	IGFX	
	PEG	
	PCI	
Select which of IGFX/PEG/PCI Graphics device should be Primary Display or select SG for Switchable Gfx.		
Internal Graphics	Auto	Default
	Disabled	
	Enabled	
Keep IGD enabled based on setup options.		
GTT Size	1MB	Default
	2MB	
Select the GTT Size.		
Aperture Size	128MB	Default
	256MB	
	512MB	
Select the Aperture Size.		

DVMT Pre-Allocated	32MB	Default
	64MB	
	96MB	
	128MB	
	160MB	
	192MB	
	224MB	
	256MB	
	288MB	
	320MB	
	352MB	
	384MB	
	416MB	
	448MB	
	480MB	
512MB		
1024MB		
Select DVMT 5.0 Pre-Allocated (Fixed) Graphics Memory size used by the Internal Graphics Device.		
DVMT Total Gfx Mem	128MB	Default
	256MB	
	MAX	
Select DVMT5.0 Total Graphic Memory size used by the Internal Graphics Device.		
Primary IGFX Boot Display	VBIOS Default	Default
	CRT	
	HDMI SKU or Display Port SKU	
	Display Port	
	DVI	
Select the Video Device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display.		

Memory Information

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Chipset

Memory Information	
Memory RC Version	1.2.2.0
Memory Frequency	1333 Mhz
Total Memory	8192 MB (DDR3)
DIMM#0	Not Present
DIMM#1	8192 MB (DDR3)
DIMM#2	Not Present
DIMM#3	Not Present
CAS Latency (tCL)	9
Minimum delay time	
CAS to RAS (tRCDmin)	9
Row Precharge (tRPmin)	9
Active to Precharge (tRASmin)	24

++: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit

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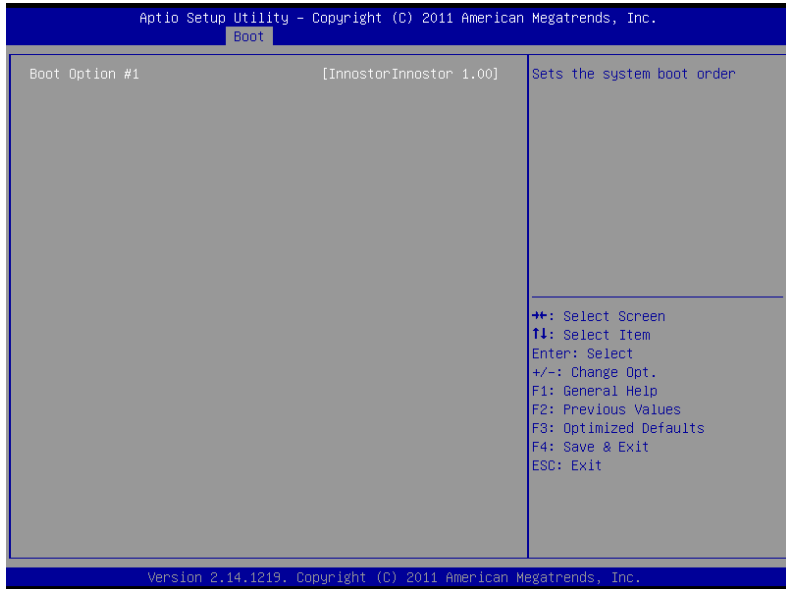
Setup submenu: Boot

Aptio Setup Utility - Copyright (C) 2011 American Megatrends, Inc.		
Main Advanced Chipset Boot Security Save & Exit		
Boot Configuration		Select the keyboard NumLock state
Bootup NumLock State	[0n]	
Quiet Boot	[Enabled]	
Launch I82579LM PXE OpROM	[Disabled]	
Launch I82583V PXE OpROM	[Disabled]	
Option ROM Messages	[Force BIOS]	
INT19 Trap Response	[Immediate]	
Boot Option Priorities		
Boot Option #1	[UEFI: InnostorInno...]	
Boot Option #2	[InnostorInnostor 1.00]	
Hard Drive BBS Priorities		
		++: Select Screen
		↑↓: Select Item
		Enter: Select
		+/-: Change Opt.
		F1: General Help
		F2: Previous Values
		F3: Optimized Defaults
		F4: Save & Exit
		ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.		

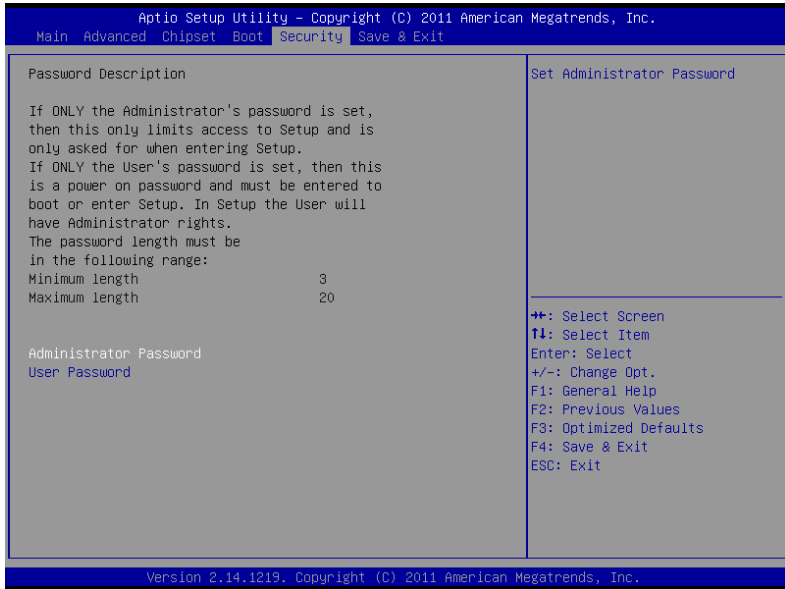
Options summary:

Bootup NumLock State	On	Default
	Off	
Select the keyboard NumLock state		
Quiet Boot	Disabled	Default
	Enabled	
En/Disable showing boot logo.		
Launch I82579LM PXE OpROM	Disabled	Default
	Enabled	
En/Disable Legacy Boot Option for I82579LM.		
Launch I82583V PXE OpROM	Disabled	Default
	Enabled	
En/Disable Legacy Boot Option for I82583V.		
Option ROM Messages	Force BIOS	Default
	Keep Current	
Set display mode for Option ROM.		
INT19 Trap Response	Immediate	Default
	Postponed	
BIOS reaction on INT19 trapping by Option ROM: IMMEDIATE – execute the trap right away; POSTPONED – execute the trap during legacy boot.		

BBS Priorities



Security



Change User/Supervisor Password

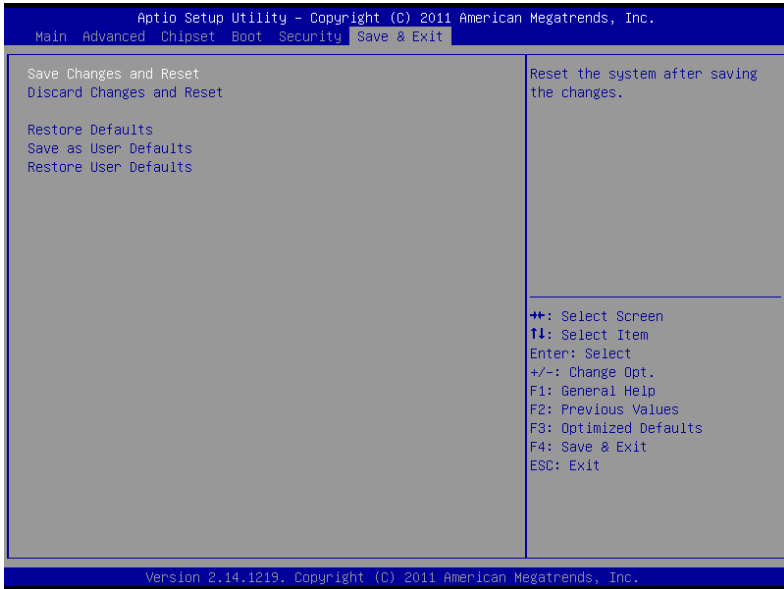
You can install a Supervisor password, and if you install a supervisor password, you can then install a user password. A user password does not provide access to many of the features in the Setup utility.

If you highlight these items and press Enter, a dialog box appears which lets you enter a password. You can enter no more than six letters or numbers. Press Enter after you have typed in the password. A second dialog box asks you to retype the password for confirmation. Press Enter after you have retyped it correctly. The password is required at boot time, or when the user enters the Setup utility.

Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

Setup submenu: Exit



Chapter

4

**Driver
Installation**

The IMBA-Q77 comes with a DVD-ROM that contains all drivers your need.

Follow the sequence below to install the drivers:

- Step 1 – Install Chipset Driver
- Step 2 – Install VGA Driver
- Step 3 – Install LAN Driver
- Step 4 – Install AUDIO Driver
- Step 5 – Install USB3.0 Driver
- Step 6 – Install RAID & AHCI Driver
- Step 7 – Install ME Driver
- Step 8 – Install TPM Driver
- Step 9 – Install UART Driver

Please read following instructions for detailed installations.

4.1 Installation:

Insert the IMBA-Q77 DVD-ROM into the DVD-ROM Drive. And install the drivers from Step 1 to Step 9 in order.

Step 1 – Install Chipset Driver

1. Click on the **Step1 - Chipset** folder and then double click on the **infinst_autol.exe**
2. Follow the instructions that the window shows
3. The system will help you to install the driver automatically

Step 2 – Install VGA Driver

1. Click on the **Step 2 - VGA** folder and select the OS your system is
2. Double click on **Setup.exe** file located in each OS folder
3. Follow the instructions that the window shows
4. The system will help you to install the driver automatically

Step 3 – Install LAN Driver

1. Click on the **Step 3 - LAN** folder and select the OS your system is
2. Double click on **.exe** file located in each OS folder
3. Follow the instructions that the window shows
4. The system will help you to install the driver automatically

Step 4 – Install AUDIO Driver

1. Click on the **Step 4 - Audio** folder and select the OS your system is

2. Double click on **Setup.exe** file located in each OS folder
3. Follow the instructions that the window shows
4. The system will help you to install the driver automatically

Step 5 – Install USB3.0 Driver

1. Click on the **Step 5 – USB3.0** folder and select the folder of **Driver_Installer**
2. Double click on **Setup.exe** file
3. Follow the instructions that the window shows
4. The system will help you to install the driver automatically

Note: USB3.0 only supports the OS of Windows 7 and above.

Step 6 – Install RAID & AHCI Driver

Please refer to Appendix D RAID & AHCI Settings

Step 7 – Install ME Driver

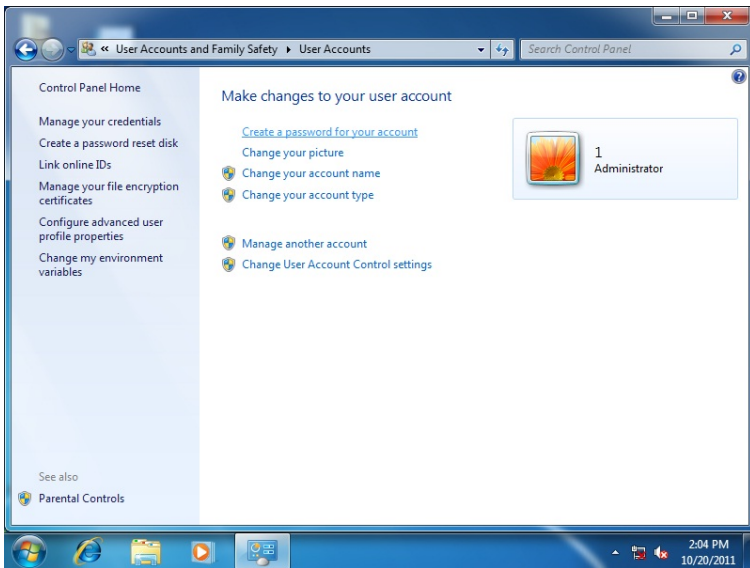
1. Click on the **Step 7 - ME** folder and double click on **setup.exe** file
2. Follow the instructions that the window shows
3. The system will help you to install the driver automatically

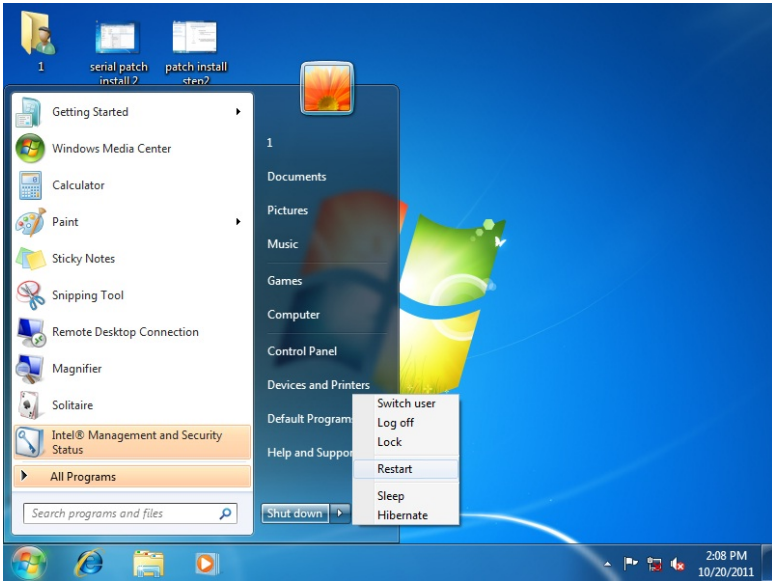
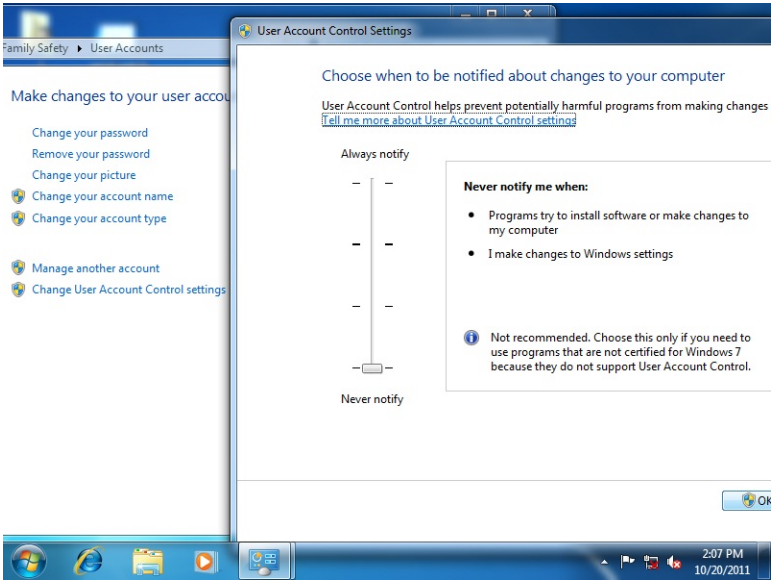
Step 8 – Install TPM Driver

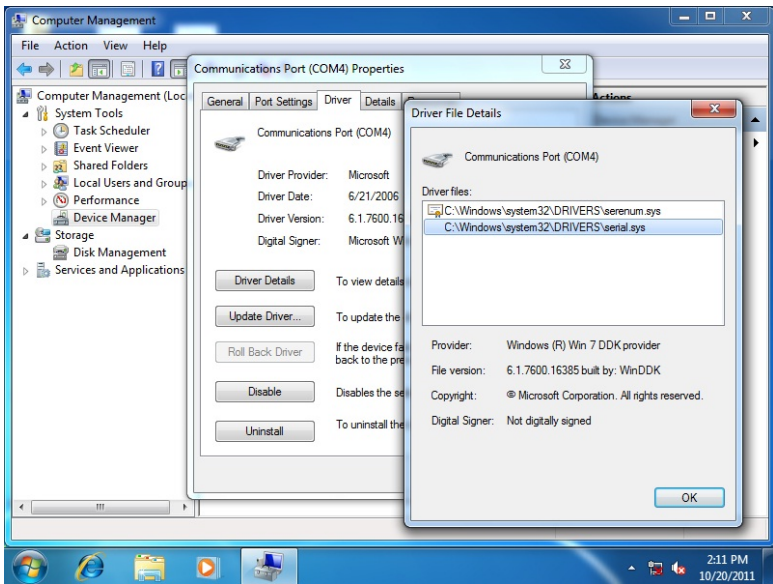
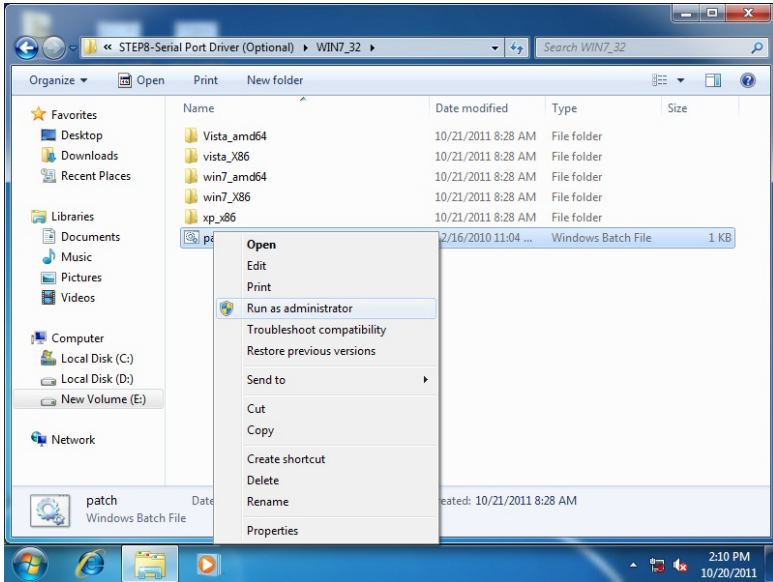
1. Click on the **Step 8 - TPM** folder and double click on **Setup.exe** file
2. Follow the instructions that the window shows

3. The system will help you to install the driver automatically

Step 9 – Install UART Driver







Appendix

A

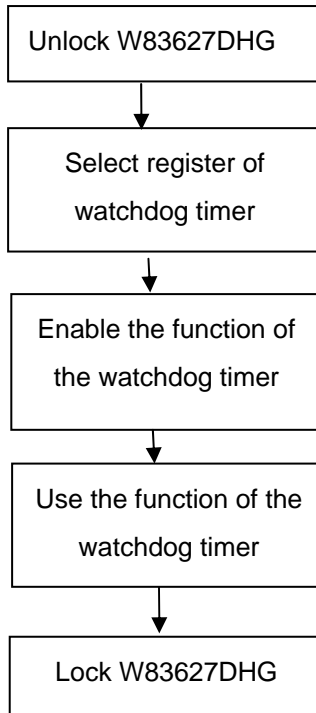
Programming the Watchdog Timer

A.1 Programming

IMBA-Q77 utilizes W83627DHG chipset as its watchdog timer controller.

Below are the procedures to complete its configuration and the AAEON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

Configuring Sequence Description



There are three steps to complete the configuration setup:

- (1) Enter the W83627DHG config Mode
- (2) Modify the data of configuration registers

- (3) Exit the W83627DHG config Mode. Undesired result may occur if the config Mode is not exited normally.

(1) Enter the W83627DHG config Mode

To enter the W83627DHG config Mode, two special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform two write operations to the Special Address port (2EH). The different enter keys are provided to select configuration ports (2EH/2Fh) of the next step.

	Address Port	Data Port
87h,87h:	2Eh	2Fh

(2) Modify the Data of the Registers

All configuration registers can be accessed after entering the config Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

(3) Exit the W83627DHG config Mode

The exit key is provided to select configuration ports (2EH/2Fh) of the next step.

	Address Port	Data Port
0aah:	2Eh	2Fh

CR 30h. (Default 02h)

BIT	READ/WRITE	DESCRIPTION
7~3	Reserved.	
2	R/W	0: GPIO6 is inactive. 1: GPIO6 is active.

1	R/W	0: GPIO5 is inactive. 1: GPIO5 is active.
0	R/W	0: WDTO# and PLED are inactive. 1: WDTO# and PLED are inactive.

CR F5h. (WDTO# and KBC P20 Control Mode Register; Default 00h)

BIT	READ/WRITE	DESCRIPTION
7~5	Reserved.	
4	R/W	1000 time faster in WDTO# count mode. 0: Disable. 1: Enable. (If bit-3 is Second Mode, the count mode is 1/1000 Sec.) (If bit-3 is Minute Mode, the count mode is 1/1000 Min.)
3	R/W	Select WDTO# count mode. 0: Second Mode. 1: Minute Mode.
2	R/W	Enable the rising edge of KBC reset (P20) to issue time-out event. 0: Disable. 1: Enable.
1	R/W	Disable/ Enable the WDTO# output low pulse to the KBRST# pin (PIN60) 0: Disable. 1: Enable.
0	Reserved.	

CR F6h. (WDTO# Counter Register; Default 00h)

BIT	READ/WRITE	DESCRIPTION
7~0	R/W	Watch Dog Timer Time-out value. Writing a non-zero value to this register causes the counter to load the value to Watch Dog Counter and start counting down. If bits 7 and 6 of CR F7h are set, any Mouse Interrupt or Keyboard Interrupt event will also cause the reload of previously-loaded non-zero value to Watch Dog Counter and start counting down. Reading this register returns current value in Watch Dog Counter instead of Watch Dog Timer Time-out value. 00h: Time-out Disable

		01h: Time-out occurs after 1 second/minute 02h: Time-out occurs after 2 second/minutes 03h: Time-out occurs after 3 second/minutes FFh: Time-out occurs after 255 second/minutes
--	--	--

CR F7h. (WDTO# Control & Status Register; Default 00h)

BIT	READ/WRITE	DESCRIPTION
7	R/W	Mouse interrupt reset watch-dog timer enable 0: Watchdog timer is not affected by mouse interrupt. 1: Watchdog timer is reset by mouse interrupt.
6	R/W	Keyboard interrupt reset watch-dog timer enable 0: Watchdog timer is not affected by keyboard interrupt. 1: Watchdog timer is reset by keyboard interrupt.
5	Write "1" Only	Trigger WDTO# event. This bit is self-clearing.
4	R/W Write "0" Clear	WDTO# status bit 0: Watchdog timer is running. 1: Watchdog timer issue time-out event.
3~0	R/W	These bits select IRQ resource for WDTO#. (02h for SMI# event.)

A.2 W83627DHG Watchdog Timer Initial Program

	LDN	Register	Bit	Description
WDT Timer value	0x07	0xF6	Bit [7-0]	00h: Time-out Disable 01h: Time-out occurs after 1 minute only. 02h: Time-out occurs after 2 second/minutes 03h: Time-out occurs after 3 second/minutes FFh: Time-out occurs after 255 second/minutes (The deviation is approx 1 second.)
WDT Unit	0x07	0xF5	Bit3	Select WDTO# count mode. 0: Second Mode. 1: Minute Mode.

```
*****
#include <stdio.h>
#include <conio.h>

#define SIOIndex    0x2E //Modify for project support 2E/4E
#define SIOData    0x2F //Modify for project support 2F/4F
#define void AaeonWDTConfig(void);
#define void AaeonWDTEnable(Byte Timer, boolean Unit);

void Main(){
    // Procedure : AaeonWDTConfig
    // This procdure will enable the WDT counting.
    AaeonWDTConfig (void);

    // Procedure : AaeonWDTEnable
    // (byte)Timer      : Time of WDT timer.(0x00~0xFF)
    // (boolean)Unit    : Select time unit(0: second, 1: minute).
    AaeonWDTEnable(Byte Timer, boolean Unit);
}

```

```
*****
// Procedure : AaeonWDTConfig
void AaeonWDTConfig (void){
    Byte val;
    //Super I/O Entry Key
    outportb(SIOIndex,0x87);
    outportb(SIOIndex,0x87);

    //Setting WDT Pin.
    outportb(SIOIndex,0x2D);
    val = inportb((SIOData);
    outportb(SIOIndex,0x2D);
    outportb(SIOData,val & 0xFE);

    // Enable WatchDog function
    outportb(SIOIndex,0x07);
    outportb(SIOData,0x08);
    outportb(SIOIndex,0x30);
    outportb(SIOData, 0x01);
}
*****
**
```

```
// Procedure :
void AaeonWDTEnable (Byte Timer, boolean Unit){
    Byte val;

    //Super I/O Entry Key
    outputb(SIOIndex,0x87);
    outputb(SIOIndex,0x87);

    // Select Logic Device Number Register
    outputb(SIOIndex,0x07);
    outputb(SIOData,0x08);

    // Setting WDT Operation Mode
    outputb(SIOIndex,0xF5);
    val = inportb((SIOData);
    outputb(SIOIndex,0xF5);
    outputb(SIOData, val | Unit << 3 );

    // Setting WDT Counter
    outputb(SIOIndex,0xF6);
    outputb(SIOData,Timer);
}
```




































Appendix

B

I/O Information

B.1 I/O Address Map

Input/output (IO)	
[00000000 - 0000001F]	Direct memory access controller
[00000000 - 00000CF7]	PCI bus
[00000010 - 0000001F]	Motherboard resources
[00000020 - 00000021]	Programmable interrupt controller
[00000022 - 0000003F]	Motherboard resources
[00000024 - 00000025]	Programmable interrupt controller
[00000028 - 00000029]	Programmable interrupt controller
[0000002C - 0000002D]	Programmable interrupt controller
[0000002E - 0000002F]	Motherboard resources
[00000030 - 00000031]	Programmable interrupt controller
[00000034 - 00000035]	Programmable interrupt controller
[00000038 - 00000039]	Programmable interrupt controller
[0000003C - 0000003D]	Programmable interrupt controller
[00000040 - 00000043]	System timer
[00000044 - 0000005F]	Motherboard resources
[0000004E - 0000004F]	Motherboard resources
[00000050 - 00000053]	System timer
[00000061 - 00000061]	Motherboard resources
[00000062 - 00000063]	Motherboard resources
[00000063 - 00000063]	Motherboard resources
[00000065 - 00000065]	Motherboard resources
[00000065 - 0000006F]	Motherboard resources
[00000067 - 00000067]	Motherboard resources
[00000070 - 00000070]	Motherboard resources
[00000070 - 00000077]	System CMOS/real time clock
[00000072 - 0000007F]	Motherboard resources
[00000080 - 00000080]	Motherboard resources
[00000080 - 00000080]	Motherboard resources
[00000081 - 00000091]	Direct memory access controller
[00000084 - 00000086]	Motherboard resources
[00000088 - 00000088]	Motherboard resources
[0000008C - 0000008E]	Motherboard resources
[00000090 - 0000009F]	Motherboard resources
[00000092 - 00000092]	Motherboard resources
[00000093 - 0000009F]	Direct memory access controller
[000000A0 - 000000A1]	Programmable interrupt controller
[000000A2 - 000000BF]	Motherboard resources
[000000A4 - 000000A5]	Programmable interrupt controller
[000000A8 - 000000A9]	Programmable interrupt controller
[000000AC - 000000AD]	Programmable interrupt controller









































	[000000B2 - 000000B3]	Motherboard resources
	[000000B4 - 000000B5]	Programmable interrupt controller
	[000000B8 - 000000B9]	Programmable interrupt controller
	[000000BC - 000000BD]	Programmable interrupt controller
	[000000C0 - 000000DF]	Direct memory access controller
	[000000E0 - 000000EF]	Motherboard resources
	[000000F0 - 000000FF]	Numeric data processor
	[00000290 - 0000029F]	Motherboard resources
	[000002C0 - 000002C7]	Communications Port (COM3)
	[000002C8 - 000002CF]	Communications Port (COM4)
	[000002D0 - 000002D7]	Communications Port (COM5)
	[000002D8 - 000002DF]	Communications Port (COM6)
	[000002F8 - 000002FF]	Communications Port (COM2)
	[00000378 - 0000037F]	Printer Port (LPT1)
	[000003B0 - 000003BB]	Intel(R) HD Graphics 4000
	[000003C0 - 000003DF]	Intel(R) HD Graphics 4000
	[000003F8 - 000003FF]	Communications Port (COM1)
	[00000400 - 00000453]	Motherboard resources
	[00000454 - 00000457]	Motherboard resources
	[00000458 - 0000047F]	Motherboard resources
	[000004D0 - 000004D1]	Motherboard resources
	[000004D0 - 000004D1]	Programmable interrupt controller
	[00000500 - 0000057F]	Motherboard resources
	[00000680 - 0000069F]	Motherboard resources
	[00000D00 - 0000FFFF]	PCI bus
	[00001000 - 0000100F]	Motherboard resources
	[0000164E - 0000164F]	Motherboard resources
	[0000E000 - 0000EFFF]	Intel(R) 7 Series/C216 Chipset Family PCI Express Root Port 7 - 1E1C
	[0000F000 - 0000F03F]	Intel(R) HD Graphics 4000
	[0000F040 - 0000F05F]	Intel(R) 7 Series/C216 Chipset Family SMBus Host Controller - 1E22
	[0000F060 - 0000F07F]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	[0000F0A0 - 0000F0A3]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	[0000F0B0 - 0000F0B7]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	[0000F0C0 - 0000F0C3]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	[0000F0D0 - 0000F0D7]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	[0000F0E0 - 0000F0E7]	Intel(R) Active Management Technology - SOL (COM5)
	[0000FFFF - 0000FFFF]	Motherboard resources
	[0000FFFF - 0000FFFF]	Motherboard resources









































B.2 1st MB Memory Address Map














Address Range	Device Name
[000A0000 - 000BFFFF]	Intel(R) HD Graphics 4000
[000A0000 - 000BFFFF]	PCI bus
[000D0000 - 000D3FFF]	PCI bus
[000D4000 - 000D7FFF]	PCI bus
[000D8000 - 000DBFFF]	PCI bus
[000DC000 - 000DFFFF]	PCI bus
[000E0000 - 000E3FFF]	PCI bus
[000E4000 - 000E7FFF]	PCI bus
[20000000 - 201FFFFFF]	System board
[40004000 - 40004FFF]	System board
[DFA00000 - DFA00FFF]	Motherboard resources
[DFA00000 - FEFFFFFF]	PCI bus
[E0000000 - EFFFFFFF]	Intel(R) HD Graphics 4000
[F7800000 - F7BFFFFF]	Intel(R) HD Graphics 4000
[F7C00000 - F7C1FFFF]	Intel(R) 82583V Gigabit Network Connection
[F7C00000 - F7CFFFFFF]	Intel(R) 7 Series/C216 Chipset Family PCI Express Root Port 7 - 1E1C
[F7C20000 - F7C23FFF]	Intel(R) 82583V Gigabit Network Connection
[F7D00000 - F7D1FFFF]	Intel(R) 82579LM Gigabit Network Connection
[F7D20000 - F7D2FFFF]	Intel(R) USB 3.0 eXtensible Host Controller
[F7D30000 - F7D33FFF]	High Definition Audio Controller
[F7D35000 - F7D350FF]	Intel(R) 7 Series/C216 Chipset Family SMBus Host Controller - 1E22
[F7D36000 - F7D367FF]	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
[F7D37000 - F7D373FF]	Intel(R) 7 Series/C216 Chipset Family USB Enhanced Host Controller - 1E26
[F7D38000 - F7D383FF]	Intel(R) 7 Series/C216 Chipset Family USB Enhanced Host Controller - 1E2D
[F7D39000 - F7D39FFF]	Intel(R) 82579LM Gigabit Network Connection
[F7D3A000 - F7D3AFFF]	Intel(R) Active Management Technology - SOL (COM5)
[F7D3C000 - F7D3C00F]	Intel(R) Management Engine Interface
[F8000000 - FBFFFFFF]	Motherboard resources
[FED00000 - FED003FF]	High precision event timer
[FED10000 - FED17FFF]	Motherboard resources
[FED18000 - FED18FFF]	Motherboard resources
[FED19000 - FED19FFF]	Motherboard resources
[FED1C000 - FED1FFFF]	Motherboard resources
[FED20000 - FED3FFFF]	Motherboard resources
[FED40000 - FED44FFF]	Trusted Platform Module 1.2
[FED45000 - FED8FFFF]	Motherboard resources
[FED90000 - FED93FFF]	Motherboard resources
[FEE00000 - FEEFFFFFF]	Motherboard resources
[FF000000 - FFFFFFFF]	Intel(R) 82802 Firmware Hub Device
[FFF00000 - FFFFFFFF]	Motherboard resources

B.3 IRQ Mapping Chart



Interrupt request (IRQ)	Description
(ISA) 0x00000000 (00)	System timer
(ISA) 0x00000003 (03)	Communications Port (COM2)
(ISA) 0x00000004 (04)	Communications Port (COM1)
(ISA) 0x00000008 (08)	System CMOS/real time clock
(ISA) 0x0000000A (10)	Communications Port (COM3)
(ISA) 0x0000000A (10)	Communications Port (COM4)
(ISA) 0x0000000A (10)	Communications Port (COM5)
(ISA) 0x0000000A (10)	Communications Port (COM6)
(ISA) 0x0000000D (13)	Numeric data processor
(ISA) 0x00000051 (81)	Microsoft ACPI-Compliant System
(ISA) 0x00000052 (82)	Microsoft ACPI-Compliant System
(ISA) 0x00000053 (83)	Microsoft ACPI-Compliant System
(ISA) 0x00000054 (84)	Microsoft ACPI-Compliant System
(ISA) 0x00000055 (85)	Microsoft ACPI-Compliant System
(ISA) 0x00000056 (86)	Microsoft ACPI-Compliant System
(ISA) 0x00000057 (87)	Microsoft ACPI-Compliant System
(ISA) 0x00000058 (88)	Microsoft ACPI-Compliant System
(ISA) 0x00000059 (89)	Microsoft ACPI-Compliant System
(ISA) 0x0000005A (90)	Microsoft ACPI-Compliant System
(ISA) 0x0000005B (91)	Microsoft ACPI-Compliant System
(ISA) 0x0000005C (92)	Microsoft ACPI-Compliant System
(ISA) 0x0000005D (93)	Microsoft ACPI-Compliant System
(ISA) 0x0000005E (94)	Microsoft ACPI-Compliant System
(ISA) 0x0000005F (95)	Microsoft ACPI-Compliant System
(ISA) 0x00000060 (96)	Microsoft ACPI-Compliant System
(ISA) 0x00000061 (97)	Microsoft ACPI-Compliant System
(ISA) 0x00000062 (98)	Microsoft ACPI-Compliant System
(ISA) 0x00000063 (99)	Microsoft ACPI-Compliant System
(ISA) 0x00000064 (100)	Microsoft ACPI-Compliant System
(ISA) 0x00000065 (101)	Microsoft ACPI-Compliant System
(ISA) 0x00000066 (102)	Microsoft ACPI-Compliant System
(ISA) 0x00000067 (103)	Microsoft ACPI-Compliant System
(ISA) 0x00000068 (104)	Microsoft ACPI-Compliant System
(ISA) 0x00000069 (105)	Microsoft ACPI-Compliant System
(ISA) 0x0000006A (106)	Microsoft ACPI-Compliant System
(ISA) 0x0000006B (107)	Microsoft ACPI-Compliant System
(ISA) 0x0000006C (108)	Microsoft ACPI-Compliant System
(ISA) 0x0000006D (109)	Microsoft ACPI-Compliant System
(ISA) 0x0000006E (110)	Microsoft ACPI-Compliant System

	(ISA) 0x0000006F (111)	Microsoft ACPI-Compliant System
	(ISA) 0x00000070 (112)	Microsoft ACPI-Compliant System
	(ISA) 0x00000071 (113)	Microsoft ACPI-Compliant System
	(ISA) 0x00000072 (114)	Microsoft ACPI-Compliant System
	(ISA) 0x00000073 (115)	Microsoft ACPI-Compliant System
	(ISA) 0x00000074 (116)	Microsoft ACPI-Compliant System
	(ISA) 0x00000075 (117)	Microsoft ACPI-Compliant System
	(ISA) 0x00000076 (118)	Microsoft ACPI-Compliant System
	(ISA) 0x00000077 (119)	Microsoft ACPI-Compliant System
	(ISA) 0x00000078 (120)	Microsoft ACPI-Compliant System
	(ISA) 0x00000079 (121)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007A (122)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007B (123)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007C (124)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007D (125)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007E (126)	Microsoft ACPI-Compliant System
	(ISA) 0x0000007F (127)	Microsoft ACPI-Compliant System
	(ISA) 0x00000080 (128)	Microsoft ACPI-Compliant System
	(ISA) 0x00000081 (129)	Microsoft ACPI-Compliant System
	(ISA) 0x00000082 (130)	Microsoft ACPI-Compliant System
	(ISA) 0x00000083 (131)	Microsoft ACPI-Compliant System
	(ISA) 0x00000084 (132)	Microsoft ACPI-Compliant System
	(ISA) 0x00000085 (133)	Microsoft ACPI-Compliant System
	(ISA) 0x00000086 (134)	Microsoft ACPI-Compliant System
	(ISA) 0x00000087 (135)	Microsoft ACPI-Compliant System
	(ISA) 0x00000088 (136)	Microsoft ACPI-Compliant System
	(ISA) 0x00000089 (137)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008A (138)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008B (139)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008C (140)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008D (141)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008E (142)	Microsoft ACPI-Compliant System
	(ISA) 0x0000008F (143)	Microsoft ACPI-Compliant System
	(ISA) 0x00000090 (144)	Microsoft ACPI-Compliant System
	(ISA) 0x00000091 (145)	Microsoft ACPI-Compliant System
	(ISA) 0x00000092 (146)	Microsoft ACPI-Compliant System
	(ISA) 0x00000093 (147)	Microsoft ACPI-Compliant System
	(ISA) 0x00000094 (148)	Microsoft ACPI-Compliant System
	(ISA) 0x00000095 (149)	Microsoft ACPI-Compliant System
	(ISA) 0x00000096 (150)	Microsoft ACPI-Compliant System

 (ISA) 0x00000097 (151)	Microsoft ACPI-Compliant System
 (ISA) 0x00000098 (152)	Microsoft ACPI-Compliant System
 (ISA) 0x00000099 (153)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009A (154)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009B (155)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009C (156)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009D (157)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009E (158)	Microsoft ACPI-Compliant System
 (ISA) 0x0000009F (159)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A0 (160)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A1 (161)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A2 (162)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A3 (163)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A4 (164)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A5 (165)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A6 (166)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A7 (167)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A8 (168)	Microsoft ACPI-Compliant System
 (ISA) 0x000000A9 (169)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AA (170)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AB (171)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AC (172)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AD (173)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AE (174)	Microsoft ACPI-Compliant System
 (ISA) 0x000000AF (175)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B0 (176)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B1 (177)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B2 (178)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B3 (179)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B4 (180)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B5 (181)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B6 (182)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B7 (183)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B8 (184)	Microsoft ACPI-Compliant System
 (ISA) 0x000000B9 (185)	Microsoft ACPI-Compliant System
 (ISA) 0x000000BA (186)	Microsoft ACPI-Compliant System
 (ISA) 0x000000BB (187)	Microsoft ACPI-Compliant System
 (ISA) 0x000000BC (188)	Microsoft ACPI-Compliant System
 (ISA) 0x000000BD (189)	Microsoft ACPI-Compliant System
 (ISA) 0x000000BE (190)	Microsoft ACPI-Compliant System

	(PCI) 0x0000000B (11)	Intel(R) 7 Series/C216 Chipset Family SMBus Host Controller - 1E22
	(PCI) 0x00000010 (16)	Intel(R) 7 Series/C216 Chipset Family USB Enhanced Host Controller - 1E2D
	(PCI) 0x00000010 (16)	Intel(R) 7 Series/C216 Chipset Family PCI Express Root Port 1 - 1E10
	(PCI) 0x00000010 (16)	Intel(R) Management Engine Interface
	(PCI) 0x00000012 (18)	Intel(R) 7 Series/C216 Chipset Family PCI Express Root Port 7 - 1E1C
	(PCI) 0x00000013 (19)	Intel(R) 7 Series/C216 Chipset Family SATA AHCI Controller - 1E02
	(PCI) 0x00000013 (19)	Intel(R) Active Management Technology - SOL (COM5)
	(PCI) 0x00000016 (22)	High Definition Audio Controller
	(PCI) 0x00000017 (23)	Intel(R) 7 Series/C216 Chipset Family USB Enhanced Host Controller - 1E26
	(PCI) 0xFFFFFFF8 (-5)	Intel(R) 82583V Gigabit Network Connection
	(PCI) 0xFFFFFFF8 (-4)	Intel(R) 82579LM Gigabit Network Connection
	(PCI) 0xFFFFFFF8 (-3)	Intel(R) USB 3.0 eXtensible Host Controller
	(PCI) 0xFFFFFFF8 (-2)	Intel(R) HD Graphics 4000

B.4 DMA Channel Assignments

-  Direct memory access (DMA)
 -  4 Direct memory access controller

Appendix

C

**Mating
Connector**

C.1 List of Mating Connectors and Cables

The table notes mating connectors and available cables.

Connector Label	Function	Mating Connector		Available Cable	Cable P/N
		Vendor	Model no		
SATA1	SATA Connector	TECHBEST	161S01-029A-L	SATA Cable	1709070800
SATA2	SATA Connector	TECHBEST	161S01-029A-L	SATA Cable	1709070800
SATA3	SATA Connector	TECHBEST	161S01-025A	SATA Cable	1709070800
SATA4	SATA Connector	TECHBEST	161S01-025A	SATA Cable	1709070800
SATA5	SATA Connector	TECHBEST	161S01-025A	SATA Cable	1709070800
SATA6	SATA Connector	TECHBEST	161S01-025A	SATA Cable	1709070800
LPT1	Parallel Port Connector	Catch Electronics	1147-000-26S	LPT Cable	1701260307
FP1	Front Panel Connector	JIH VEI Electronics	21B22564-XS10B-01G-6/3-VXX		N/A
FP2	Front Panel Connector	JIH VEI Electronics	21B22564-XS10B-01G-6/3-VXX		N/A
USB1	USB Connector	JIH VEI Electronics	21B22564-10S10B-01G-6/3-V10	USB Cable	1709100204
USB2	USB Connector	JIH VEI Electronics	21B22564-10S10B-01G-6/3-V10	USB Cable	1709100204
USB3	USB Connector	JIH VEI Electronics	21B22564-10S10B-01G-6/3-V10	USB Cable	1709100204
USB4	USB 3.0 Connector	PINREX	52X-40-20GV52		

COM2	COM Port Connector	Catch Electronics	1147-000-10 S	Serial Port Cable	1701100305
COM3	COM Port Connector	Catch Electronics	1147-000-10 S	Serial Port Cable	1701100305
COM4	COM Port Connector	Catch Electronics	1147-000-10 S	Serial Port Cable	1701100305
COM5	COM Port Connector	Catch Electronics	1147-000-10 S	Serial Port Cable	1701100305
COM6	COM Port Connector	Catch Electronics	1147-000-10 S	Serial Port Cable	1701100305
IR1	IrDA Connector	JIH VEI Electronics	21B12050-X XS10B-01G-4/2.8		N/A
DIO1	DIO Port Connector	Catch Electronics	1147-000-10 S		N/A
ATX1	ATX 4PIN Connector	Catch Electronics	1121-700-04 S		N/A
ATX2	ATX 24PIN Connector	Catch Electronics	1121-700-24 S		N/A
CPU_FAN	FAN Connector	Catch Electronics	1190-700-042		N/A
SYS_FAN1	FAN Connector	Catch Electronics	1190-700-042		N/A
SYS_FAN2	FAN Connector	Catch Electronics	1190-700-042		N/A
PCIE_1	PCIE X 16 Connector	TECHBEST	WPCS-164A N1B22UWL		N/A
PCIE_2	PCIE X 4 Connector	FOXCONN	2EG03217-D 2D-DF		N/A
PCIE_3	PCIE X 1 Connector	FOXCONN	2EG01817-D 2D-DF		N/A
PCIE_4	PCIE X 1 Connector	FOXCONN	2EG01817-D 2D-DF		N/A
DIMM1	DDR3 204PIN SKT	KORTAK	AR240H-101 B-A0H		N/A

DIMM2	DDR3 204PIN SKT	KORTAK	AR240H-031 B-A0H		N/A
DIMM3	DDR3 204PIN SKT	KORTAK	AR240H-101 B-A0H		N/A
DIMM4	DDR3 204PIN SKT	KORTAK	AR240H-031 B-A0H		N/A
PCI1	PCI Connector	FOXCONN	EH06001-HH W-DF		N/A
PCI2	PCI Connector	FOXCONN	EH06001-HH W-DF		N/A
PCI3	PCI Connector	FOXCONN	EH06001-HH W-DF		N/A
KBMS1	Keyboard & Mouse	FOXCONN	MH11061-P3 6-4F		N/A
DVI/COM1	COM1+DVI	TechBast	D205D1B010 12PN		N/A
DP1	DisplayPort	FOXCONN	3VD21203-H 7U0-4H		N/A
DP2	DisplayPort	KORTAK	9S020F-03A S-00H		N/A
HDMI1	HDMI	LOTES	GSP-ABA-H DM-013-K09		N/A
USB_LAN1	Dual USB & LAN	FOXCONN	JFM38U1B-2 1U5-4F		N/A
USB_LAN2	Dual USB 3.0 & LAN	UDE	05-000939M 23-1		N/A
AUDIO1	AUDIO Connector	FOXCONN	JA33331-211 9-4F		N/A

Appendix

D

RAID & AHCI Settings

D.1 Setting RAID

OS installation to setup RAID Mode

Step 1: Copy the files below from “*Driver CD -> Step 6-RAID & AHCI -> f6flpy-x86*” to Disk



PCReadme
文字文件
67 KB



iaAHCI
安全性目錄
8 KB



iaAHCI
安裝資訊
5 KB



iaStor
安全性目錄
8 KB



iaStor
安裝資訊
4 KB



iaStor
系統檔案
453 KB



license
文字文件
10 KB



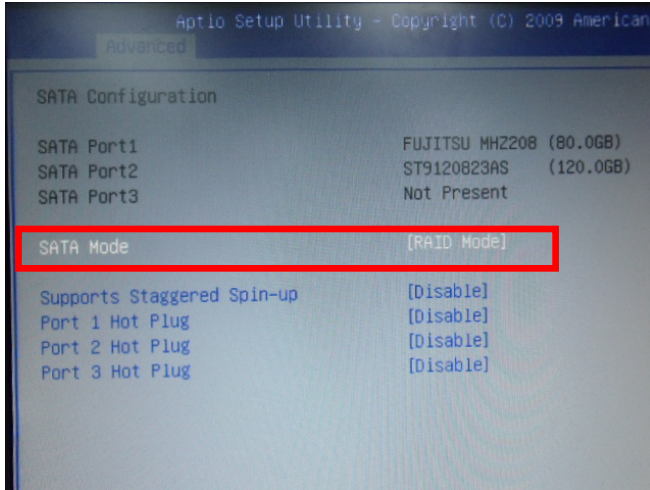
TX1SETUP.OEM
OEM 檔案
6 KB

Step 2: Connect the USB Floppy (disk with RAID files) to the board



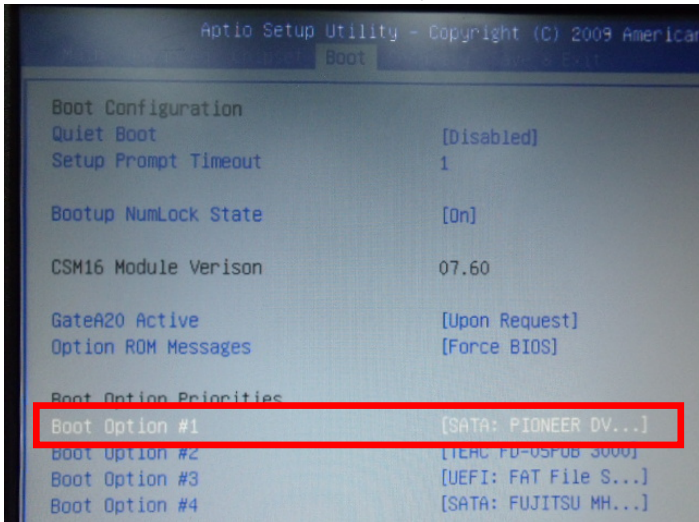
Step 3: The setting procedures "In BIOS Setup Menu"

A: Advanced -> SATA Configuration -> SATA Mode -> RAID Mode



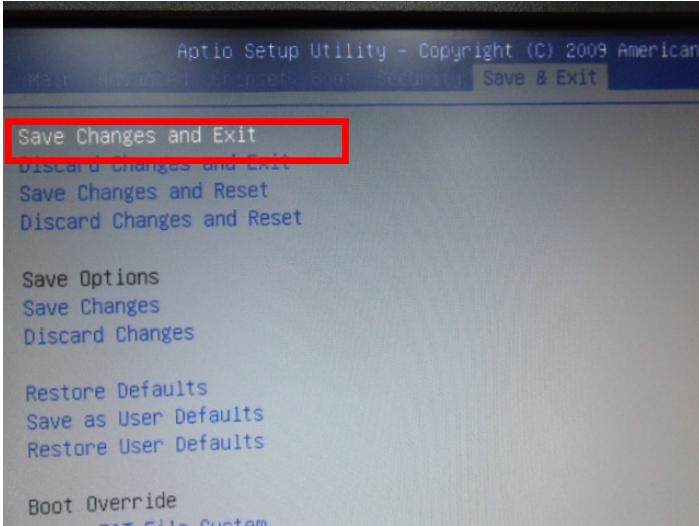
Step 4: The setting procedures "In BIOS Setup Menu"

C: Boot -> Boot Option #1 -> DVD-ROM Type

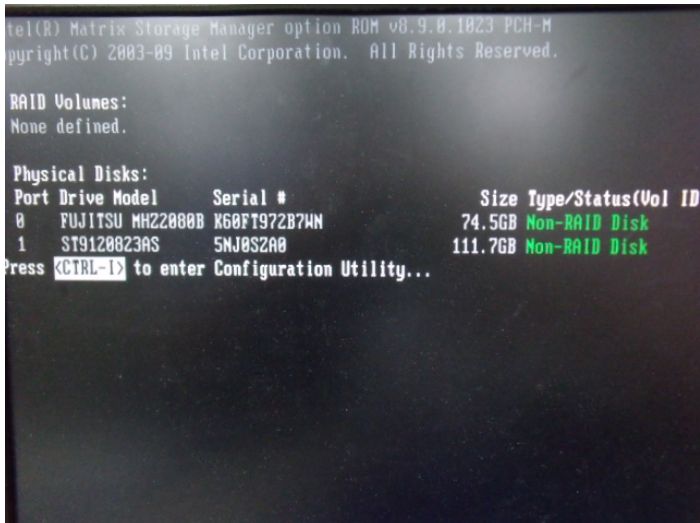


Step 5: The setting procedures "In BIOS Setup Menu"

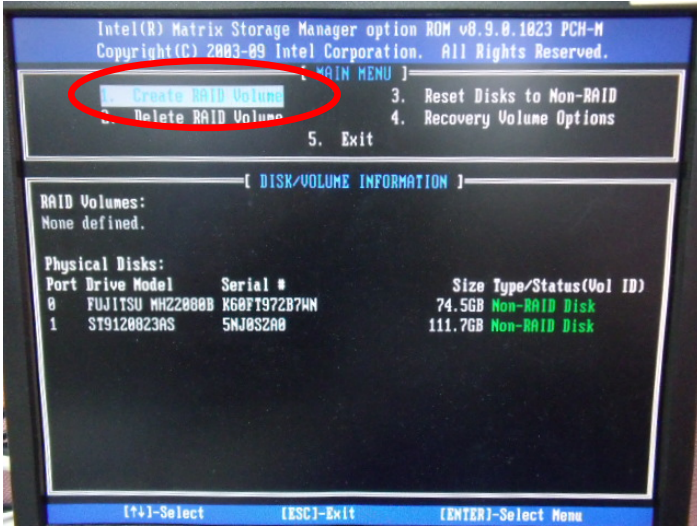
D: Save & Exit -> Save Changes and Exit



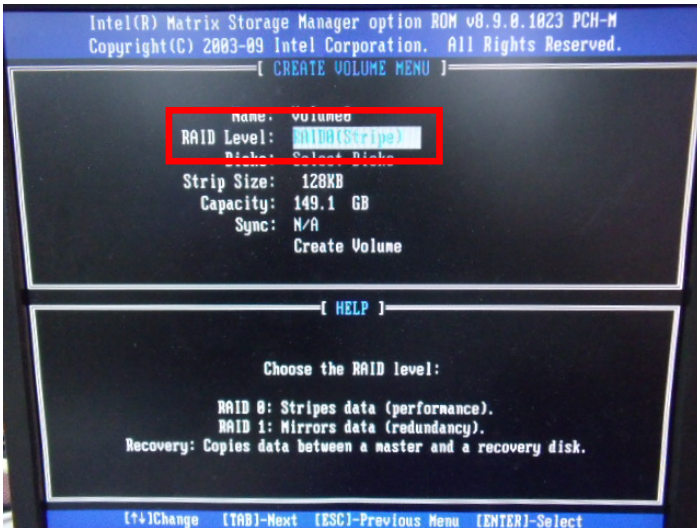
Step 6: Press **Ctrl-I** to enter **MAIN MENU**



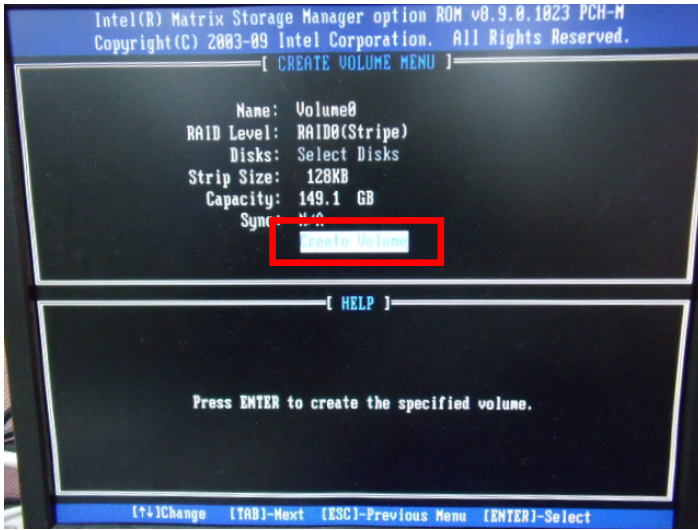
Step 7: Choose "1.Create RAID Volume"



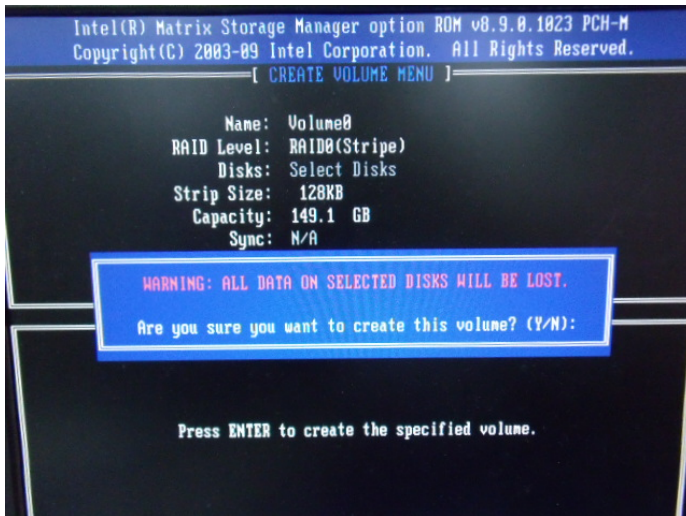
Step 8: RAID Level -> RAID0(Stripe)



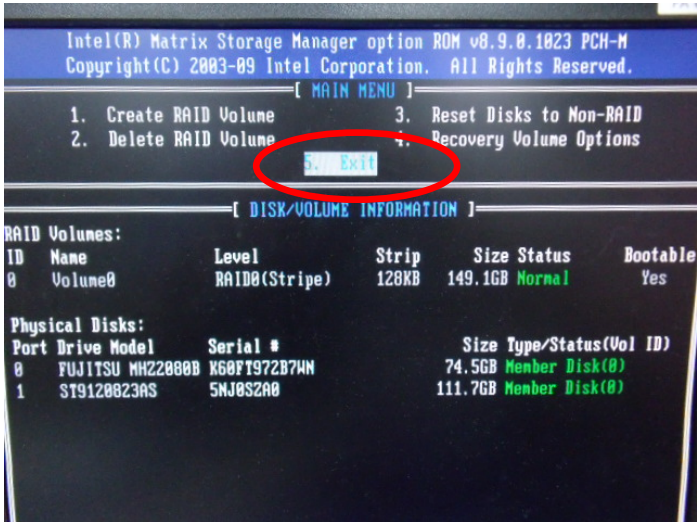
Step 9: Choose “Create Volume”



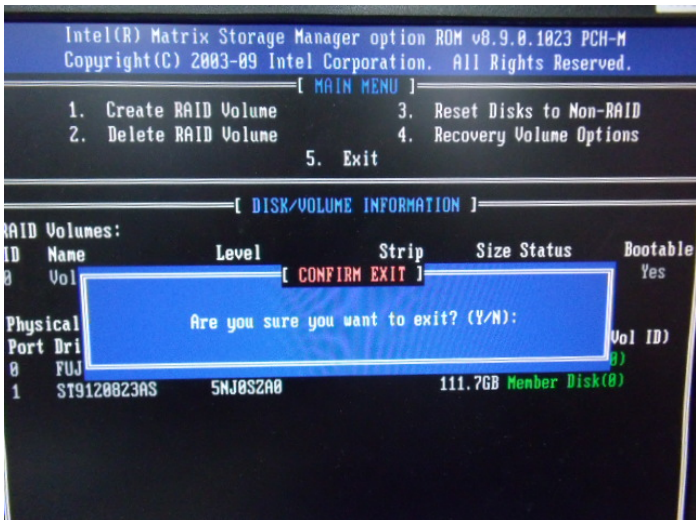
Step 10: Choose “Y”



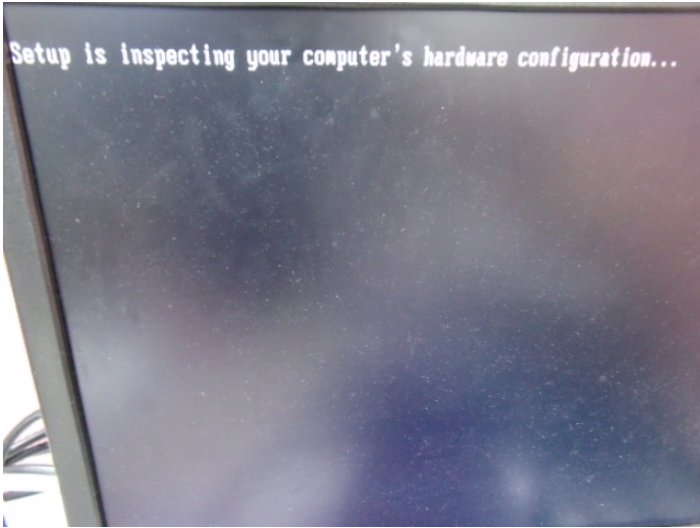
Step 11: Choose "5. Exit"



Step 12: Choose "Y"



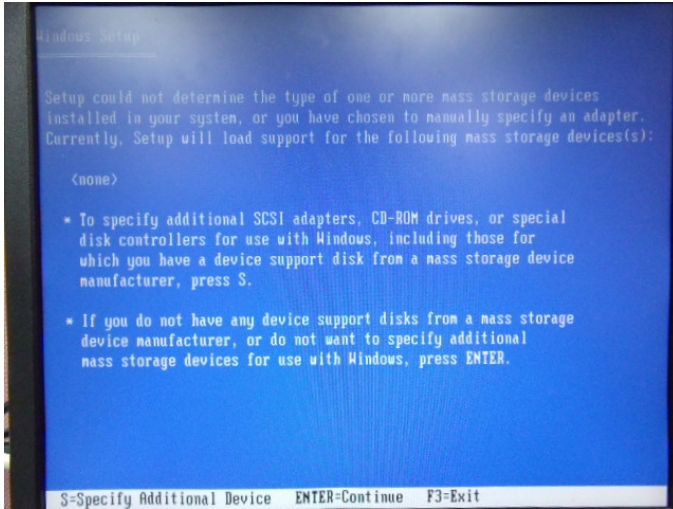
Step 13: Setup OS



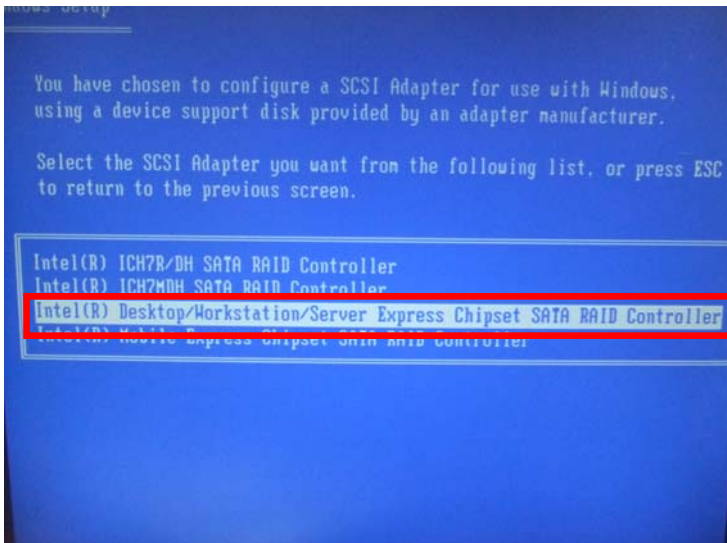
Step 14: Press "F6"



Step 15: Choose “S”



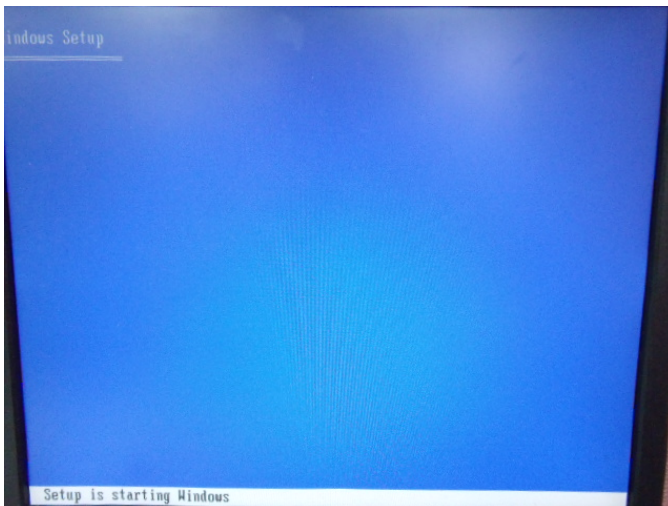
Step 16: Choose “Intel (R) Desktop/Workstation/Server Express Chipset SATA RAID Controller”



Step 17: It will show the model number you select and then press “ENTER”



Step 18: Setup is starting Windows



D.2 Setting AHCI

OS installation to setup AHCI Mode

Step 1: Copy the files below from “**Driver CD -> Step 6-RAID & AHCI -> f6flpy-x86**” to Disk

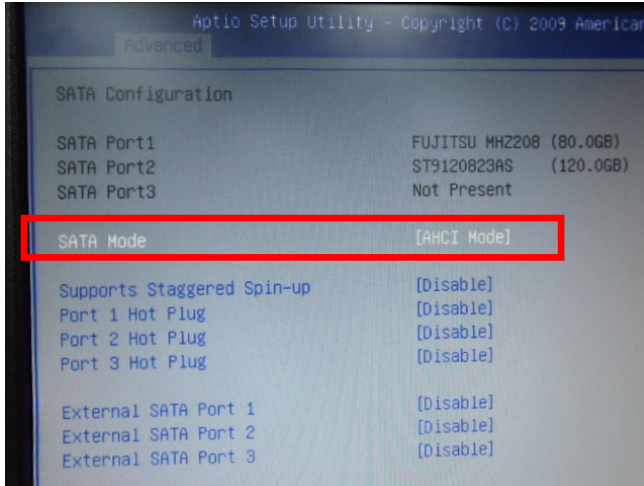


Step 2: Connect the USB Floppy (disk with RAID files) to the board



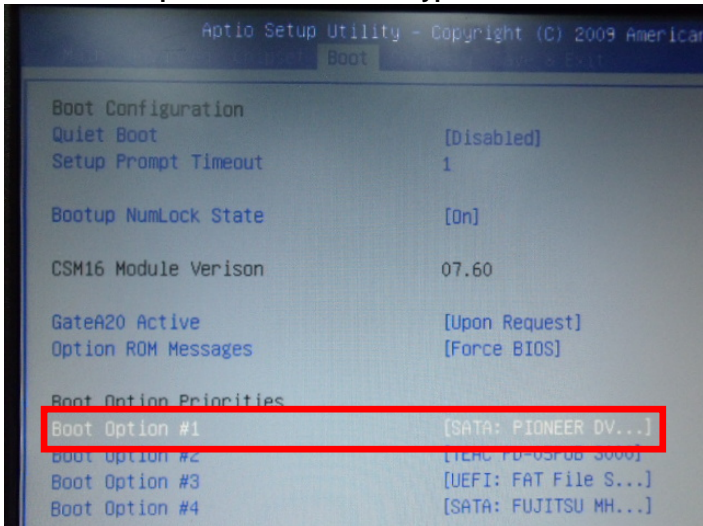
Step 3: The setting procedures “In BIOS Setup Menu”

A: Advanced -> SATA Configuration -> SATA Configuration -> SATA Mode -> AHCI Mode



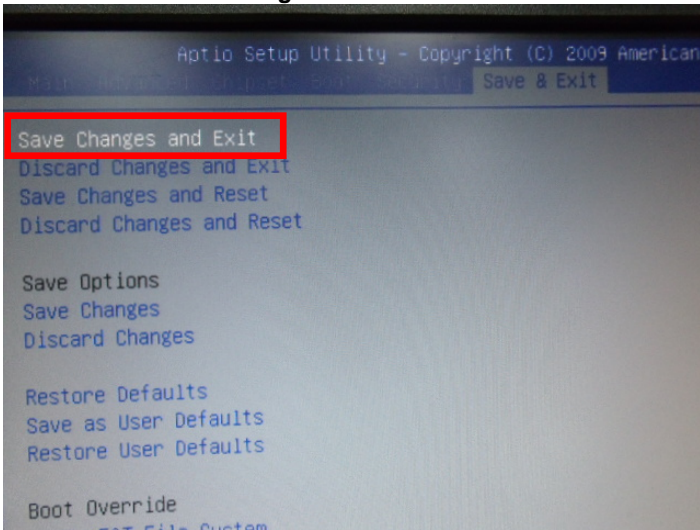
Step 4: The setting procedures “In BIOS Setup Menu”

B: Boot -> Boot Option #1 -> DVD-ROM Type

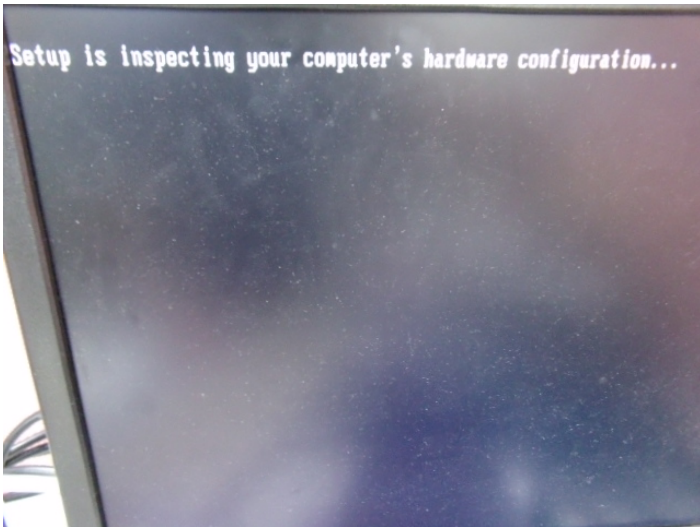


Step 5: The setting procedures "In BIOS Setup Menu"

C: Save & Exit -> Save Changes and Exit



Step 6: Setup OS



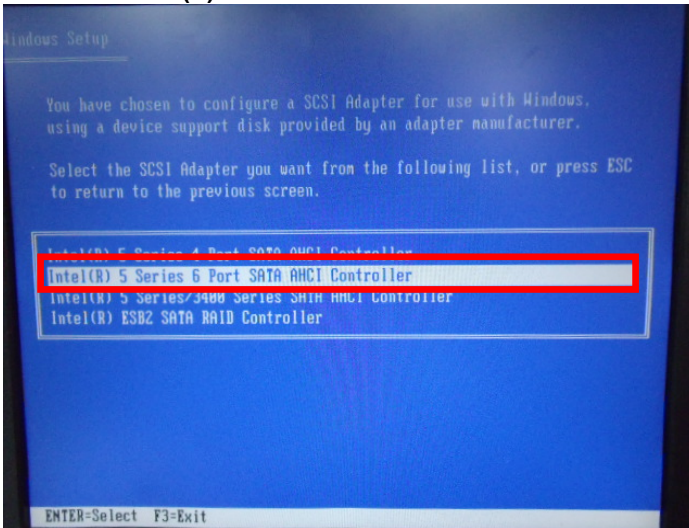
Step 7: Press "F6"



Step 8: Choose "S"



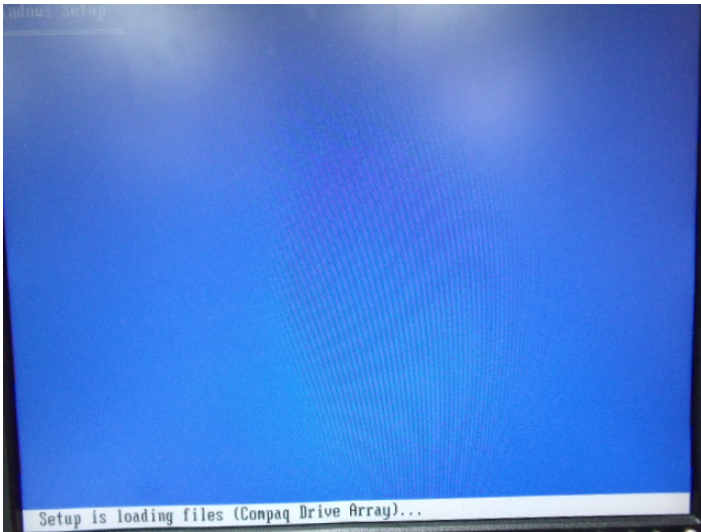
Step 9: Choose "Intel(R) 5 Series 6 Port SATA AHCI Controller"



Step 10: It will show the model number you select and then press "ENTER"



Step 11: Setup is loading files



Appendix

E

Digital Input & Output

E.1 DIO Programming

IMBA-Q77 utilizes W83627DHG chipset as its Digital I/O controller. Below are the procedures to complete its configuration and the AAeon initial watchdog timer program is also attached based on which you can develop customized program to fit your application. There are three steps to complete the configuration setup: (1) Enter the MB PnP Mode; (2) Modify the data of configuration registers; (3) Exit the MB PnP Mode. Undesired result may occur if the MB PnP Mode is not exited normally. (These three steps are the same as programming WDT)

E.2 Digital I/O Register

GPIO Device Configuration Register (LDN 0x09)

Register 0x[HEX]	Register Name
F0	DIO I/O register 0: The respective DIO PIN is programmed as an output port. 1: The respective DIO PIN is programmed as an input port.
F1	GPIO3 Data register For output ports, the respective bits can be read and written by the pins. For input ports , the respective bits can only be read by the pins. Write accesses are ignored.

E.3 Digital I/O Sample Program

Digital Input/Output register table

Digital Input Pin Status			
	LDN	Register	Bit
DIO-1(GPIO30)	0x09	0xF0	Bit0
DIO-2(GPIO31)	0x09	0xF0	Bit1
DIO-3(GPIO32)	0x09	0xF0	Bit2
DIO-4(GPIO33)	0x09	0xF0	Bit3
DIO-5(GPIO34)	0x09	0xF0	Bit4
DIO-6(GPIO35)	0x09	0xF0	Bit5
DIO-7(GPIO36)	0x09	0xF0	Bit6
DIO-8(GPIO37)	0x09	0xF0	Bit7

Digital Output Pin output High/Low			
	LDN	Register	Bit
DIO-1(GPIO30)	0x09	0xF1	Bit0
DIO-2(GPIO31)	0x09	0xF1	Bit1
DIO-3(GPIO32)	0x09	0xF1	Bit2
DIO-4(GPIO33)	0x09	0xF1	Bit3
DIO-5(GPIO34)	0x09	0xF1	Bit4
DIO-6(GPIO35)	0x09	0xF1	Bit5
DIO-7(GPIO36)	0x09	0xF1	Bit6
DIO-8(GPIO37)	0x09	0xF1	Bit7

```

*****
#include <stdio.h>
#include <conio.h>

#define SIOIndex    0x2E //Modify for project support 2E/4E
#define SIOData     0x2F //Modify for project support 2F/4F
#define boolean AaeonDigitalInput(int byte LDN, int byte RegNum, int byte
BitNum);
#define void AaeonDigitalOutput(int byte LDN, int byte RegNum, int byte
BitNum, boolean Status);
*****

Main(){
// Procedure : AaeonDigitalInput
// Input :
// (byte)LDN :          Logic Device Number Register
// (byte)RegNum : 0x00 ~ 0xFF
//                SuperIO register offset that you want to access
// (byte)BitNum : 0 ~ 7
//                Which bit in register that you want to access
// Output :
// (boolean)Status : When read Digital Input Status, it will return the result
                of Pin
//                Status.

boolean Status = AaeonDigitalInput(int byte LDN, int byte RegNum, int byte
BitNum);

// Procedure : AaeonDigitalOutput
// Input :
// (byte)LDN :          Logic Device Number Register
// (byte)RegNum : 0x00 ~ 0xFF
//                SuperIO register offset that you want to access
// (byte)BitNum : 0 ~ 7
//                Which bit in register that you want to access
// (boolean)Status :    When write Digital Output Status, this value is
the Pin status
//                of Digital Output that you want.

```

```
AaeonDigitalOutput(int byte LDN, int byte RegNum, int byte BitNum,  
boolean Status);  
}
```

```
// Procedure : AaeonDigitalInput
boolean AaeonDigitalInput(int byte LDN, int byte RegNum, int byte
BitNum){
    int byte ByteTempValue0 = 0;

    // Enter SIO MB PnP mode
    outputb (SIOIndex, 0x87);
    outputb (SIOIndex, 0x87);

    // Select Logic Device Number Register
    outputb (SIOIndex, 0x07); // SIO LDN Register Offset = 0x07
    outputb (SIOData, LDN);

    // Select register offset, and read the register value
    outputb (SIOIndex, RegNum);
    ByteTempValue0 = inportb (SIOData);

    // Exit SIO MB PnP mode
    outputb (SIOIndex, 0xAA);

    ByteTempValue0 &= (1 << BitNum);
    if(ByteTempValue0)
    { return 1; }
    else
    { return 0; }
}
```
