ACP-2153

Onboard Intel® Atom™ D2550
1.86 GHz Processor
Multi-Touch Panel PC
With 15” TFT LCD

ACP-2153 Manual 1st Ed.
December 2012

Copyright Notice

Acknowledgments

Packing List

Before you begin operating your PC, please make sure that the following materials are enclosed:

- ACP-2153 Touch Panel PC
- Mounting brackets and screws
- CD-ROM for manual (in PDF format) and drivers

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

Safety & Warranty

1. Read these safety instructions carefully.
2. Keep this user's manual for later reference.
3. Disconnect this equipment from any AC outlet before cleaning. Do not use liquid or spray detergents for cleaning. Use a damp cloth.
4. For pluggable equipment, the power outlet must be installed near the equipment and must be easily accessible.
5. Keep this equipment away from humidity.
6. Put this equipment on a firm surface during installation. Dropping it or letting it fall could cause damage.
7. The openings on the enclosure are for air convection. Protect the equipment from overheating. DO NOT COVER THE OPENINGS.
8. Make sure the voltage of the power source is correct before connecting the equipment to the power outlet.
9. Position the power cord so that people cannot step on it. Do not place anything over the power cord.
10. All cautions and warnings on the equipment should be noted.
11. If the equipment is not used for a long time, disconnect it from the power source to avoid damage by transient over-voltage.
12. Never pour any liquid into an opening. This could cause fire or electrical shock.
13. Never open the equipment. For safety reasons, only qualified service personnel should open the equipment.
14. If any of the following situations arises, get the equipment checked by service personnel:
    a. The power cord or plug is damaged.
    b. Liquid has penetrated into the equipment.
    c. The equipment has been exposed to moisture.

d. The equipment does not work well, or you cannot get it to work according to the user's manual.

e. The equipment has been dropped and damaged.

f. The equipment has obvious signs of breakage.

15. DO NOT LEAVE THIS EQUIPMENT IN AN ENVIRONMENT WHERE THE STORAGE TEMPERATURE IS BELOW -20°C (-4°F) OR ABOVE 60°C (140°F). IT MAY DAMAGE THE EQUIPMENT.

FCC

This device complies with Part 15 FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received including interference that may cause undesired operation.

Caution:
There is a danger of explosion if the battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions and your local government's recycling or disposal directives.

Below Table for China RoHS Requirements
产品中有毒有害物质或元素名称及含量

AAEON Panel PC/ Workstation

部件名称	有毒有害物质或元素					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
印刷电路板及其电子组件	×	○	○	○	○	○
外部信号连接器及线材	×	○	○	○	○	○
外壳	×	○	○	○	○	○
中央处理器与内存	×	○	○	○	○	○
硬盘	×	○	○	○	○	○
液晶模块	×	○	○	○	○	○
光驱	×	○	○	○	○	○
触控模块	×	○	○	○	○	○
电源	×	○	○	○	○	○

O：表示该有毒有害物质在该部件所有均质材料中的含量均在 **SJ/T 11363-2006** 标准规定的限量要求以下。

X：表示该有毒有害物质至少在该部件的某一均质材料中的含量超出 **SJ/T 11363-2006** 标准规定的限量要求。

备注：
一、此产品所标示之环保使用期限，系指在一般正常使用状况下。
二、上述部件物质中央处理器、内存、硬盘、光驱、触控模块为选购品。

Contents

Chapter

1

General Information

1.1 Introduction

The ACP-2153 operator panel is an Intel® Atom™ D2550 1.86 GHz processor computer that is designed to serve as a human machine interface (HMI). It is a PC-based system with 15" color TFT LCD display, onboard Ethernet controller, multi-COM port interfaces and an audio controller. With a built-in CompactFlash socket, the ACP-2153 is as compact and user friendly as a multi-function computer. In addition, its "fit anywhere" design makes it very flexible and able to be used in many different kinds of installations. It can be VESA 75/100 wall mounted and flush mounted.

For system integrators, this simple, complete, compact and highly integrated system let you easily build an operator panel into your applications. Common industrial applications include factory automation systems, precision machinery, and production process control. It is also suitable for many non-industrial applications, including vending machine, and car park automation. Our operator panel is a reliable, cost-effective solution to your application's processing requirements.

1.2 Specification

System

● CPU	Onboard Intel® Atom™ D2550 1.86 GHz Processor
● System Memory	DDR3 SODIMM x 1, Max. 4 GB
● Ethernet	10/100/1000Base-T, RJ-45 x 2
● LCD / CRT Controller	Intel® NM10
● I/O Port	USB2.0 x 4 RS-232 x 2 RS-232/422/485 x 1 RJ-45 x 2 VGA out x 1 Line-out x 1 Power switch x 1 Power input 3-pin terminal block x 1
● Storage Disk Drive	CFast™ slot x 1 2.5” SATA Hard Disk Drive x 1
● Expansion Slot	Mini PCIe Card x 1
● OS Support	Windows® XP, Windows® 7, Linux Fedora

Mechanical

● Construction	Open Frame
● Mounting	VESA 75/100, Flush mount

Dimension	15.06"(W) x 10.75"(H) x 2.75"(D) (382.60mm x 273.06mm x 69.80mm)
Carton Dimension	20.67"(W) x 18.31"(H) x 9.1"(D) (525mm x 465mm x 230mm)
Net Weight	11.45 lb (5.2kg)
Gross Weight	16.52 lb (7.5kg)

Environmental

Operating Temperature	-4°F~140°F (-20°C~60°C)
Storage Temperature	-4°F~158°F (-20°C~70°C)
Operating Humidity	10% to 90%@ 40°C, non-condensing
Vibration	1 g rms/ 5-500Hz/ Operation (with HDD)
Shock	20 G peak acceleration (11 msec. duration) (with HDD)
EMC	CE/FCC Class A

Power Supply

DC Input	9~30V 3-pin terminal block

LCD

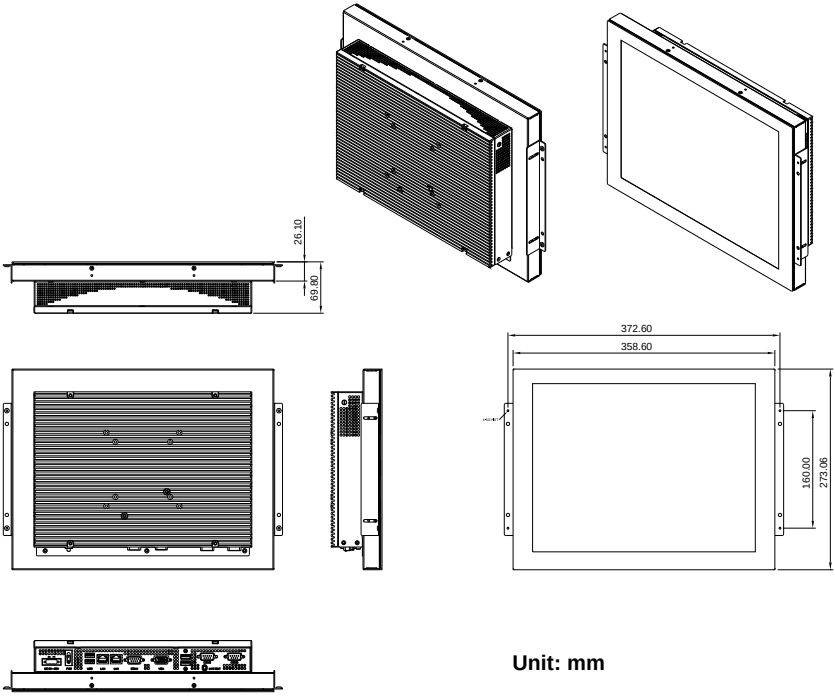
Display Type	15” TFT LCD
Max. Resolution	1024x768
Max. Colors	16.7M colors (6/8-bit for R, G, B)
Luminance (cd/m^2)	400
Viewing Angle	160° (H),140° (V)

- Backlight LED
- Backlight MTBF (Hours) 50,000

Touch Screen

- Type Projected capacitive touch screen (two points)
- Light Transmission 90%
- Lifetime N/A

1.3 Dimension



Unit: mm

Chapter

2

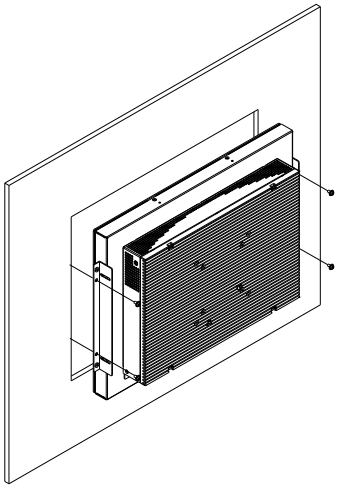
Hardware Installation

2.1 Panelmount Installation

The display panel can be mounted into the wall. You will need the screws along with the mounting brackets, which be packed in the accessory box. Follow the steps below:

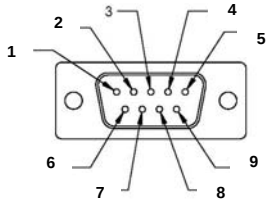
Before you start to follow the instructions, please place the display panel into the wall. See the following illustration on the left.

Complete Illustration



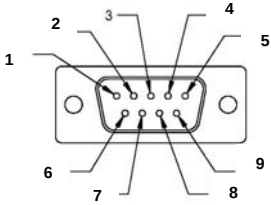
2.2 COM1/2 RS-232/422/485 Serial Port Connector

COM1 (D-sub 9 male)



Pin	Signal	Pin	Signal
1	DCD	2	RXD
3	TXD	4	DTR
5	GND	6	DSR
7	RTS	8	CTS
9	RI		

COM2 RS-232/422/485 (D-sub 9 male)



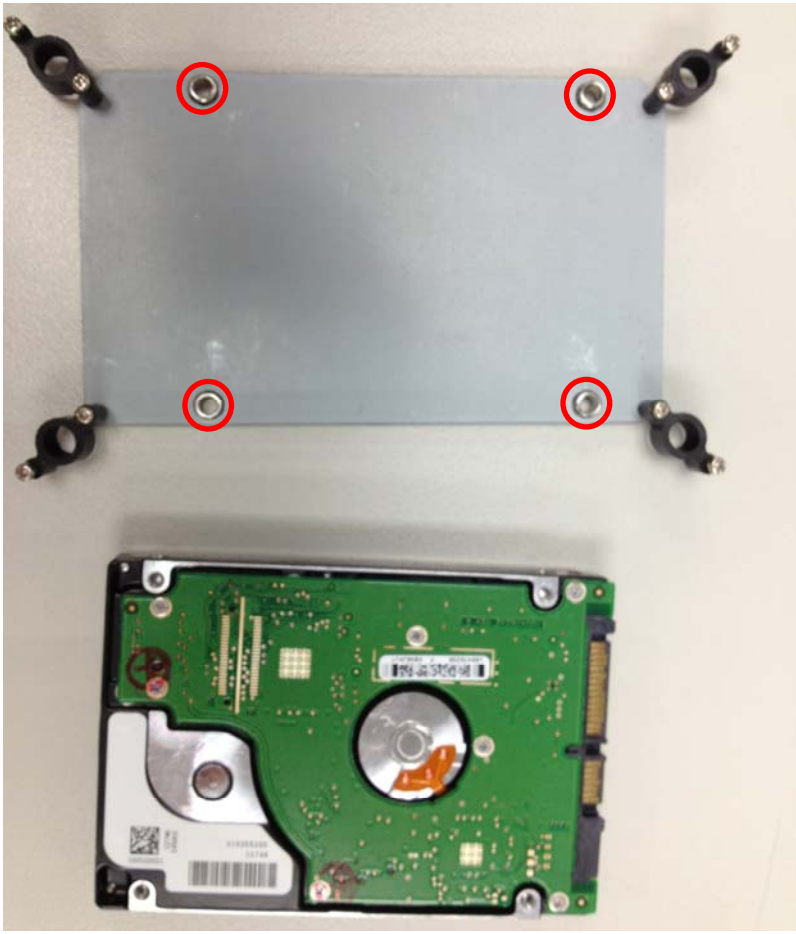
Pin	Signal	Pin	Signal
1	DCD (422TXD-/485DATA-)	2	RXD (422RXD+)
3	TXD (422TXD+/485DATA+)	4	DTR (422RXD-)
5	GND	6	DSR
7	RTS	8	CTS
9	RI/+5Volt/+12Volt		

2.3 Hard Disk Drive Installation

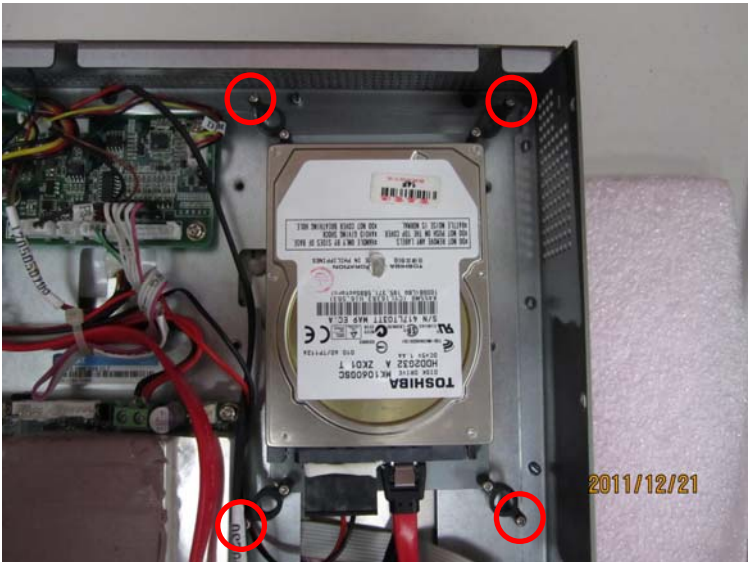
Step 1: Unfasten the screws of the heatsink

Step 2: Get the Bracket of Hard Disk Drive from the package

Step 3: Fasten the Hard Disk onto the bracket



Step 4: Fasten the screws of the hard disk bracket onto the ACP-2153



Chapter

3

AMI BIOS Setup

3.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors.

System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The CMOS memory has lost power and the configuration information has been erased.

The ACP-2153 CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

3.2 AMI BIOS Setup

AMI BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

Entering Setup
Power on the computer and press <Del> or <F2> immediately. This will allow you to enter Setup.

Main
Set the date, use tab to switch between date elements.

Advanced
Enable disable boot option for legacy network devices.

Chipset
Host bridge parameters.

Boot
Enables/disable quiet boot option.

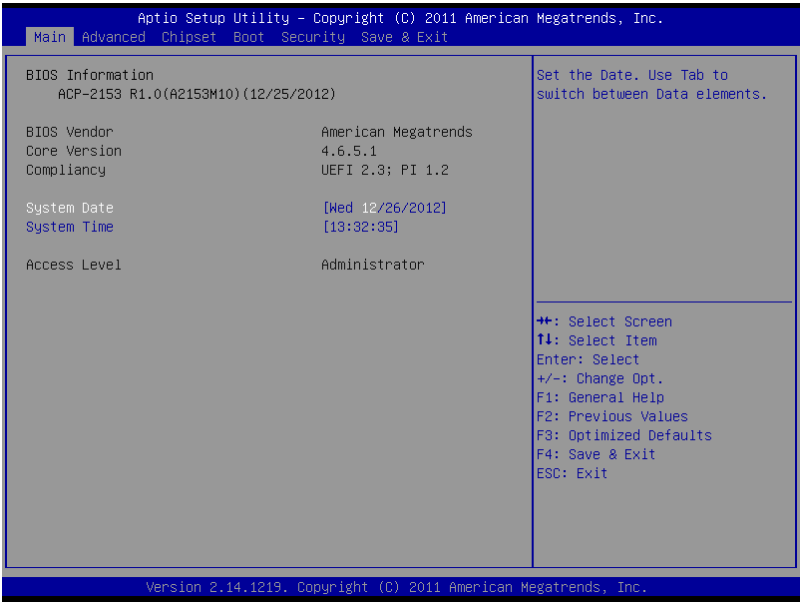
Security
Set setup administrator password.

Save&Exit
Exit system setup after saving the changes.

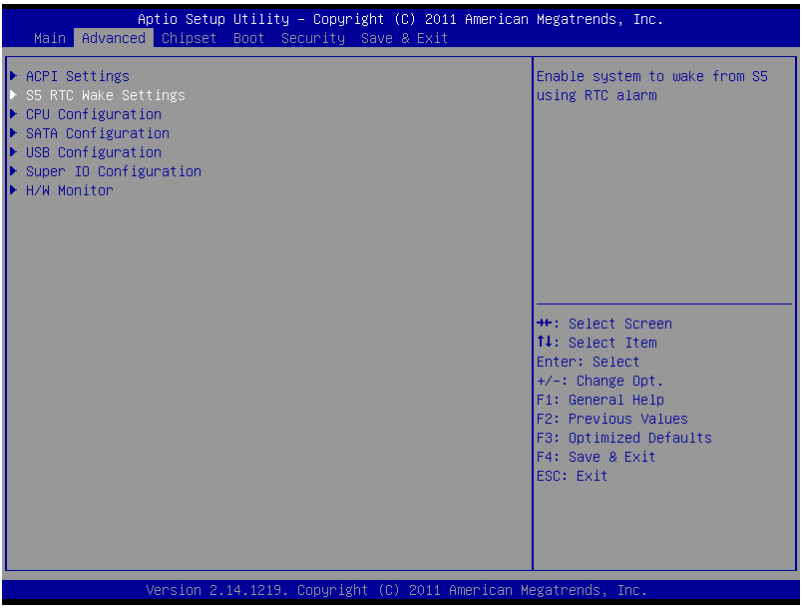
Setup Menu

Main Setup Menu

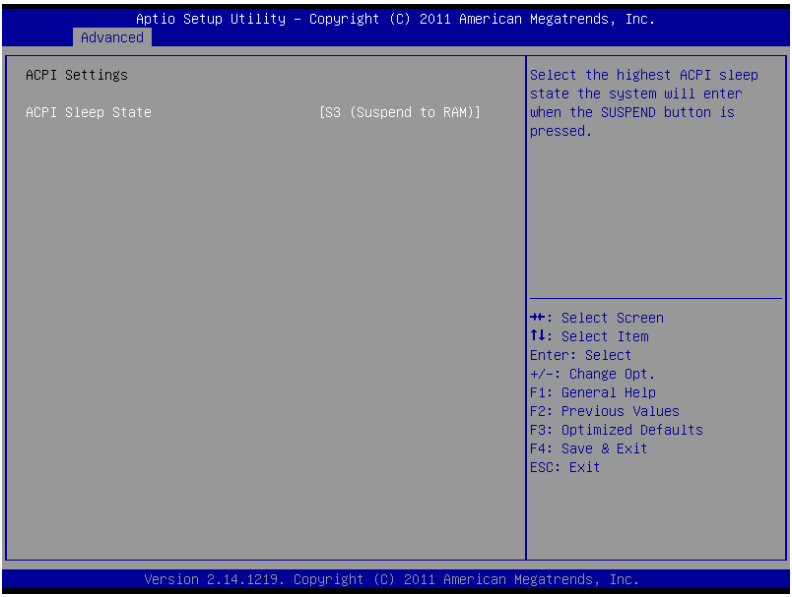
Press '*Delete*' Key to enter Setup



Setup submenu: Advanced



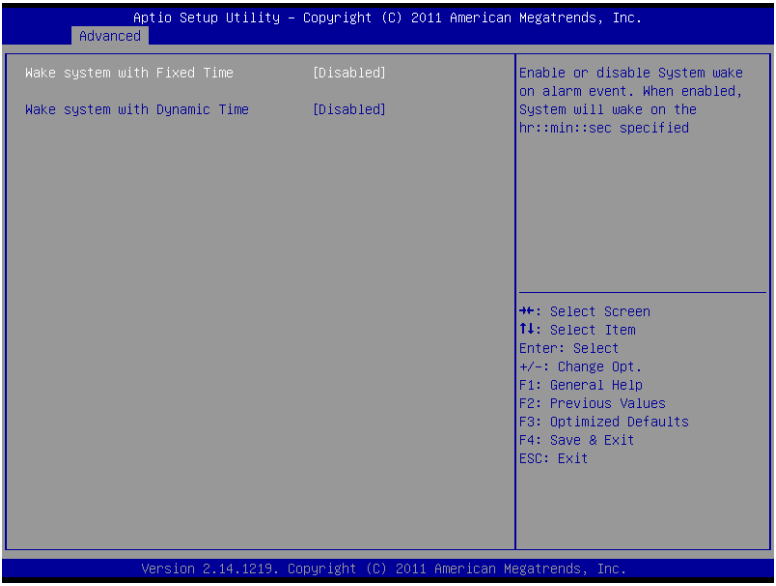
ACPI Settings



Options summary:

Suspend mode	Suspend Disabled S1 (CPU Stop Clock)	
	S3 (Suspend to RAM)	Optimal Default, Failsafe Default
Select the ACPI state used for System Suspend		

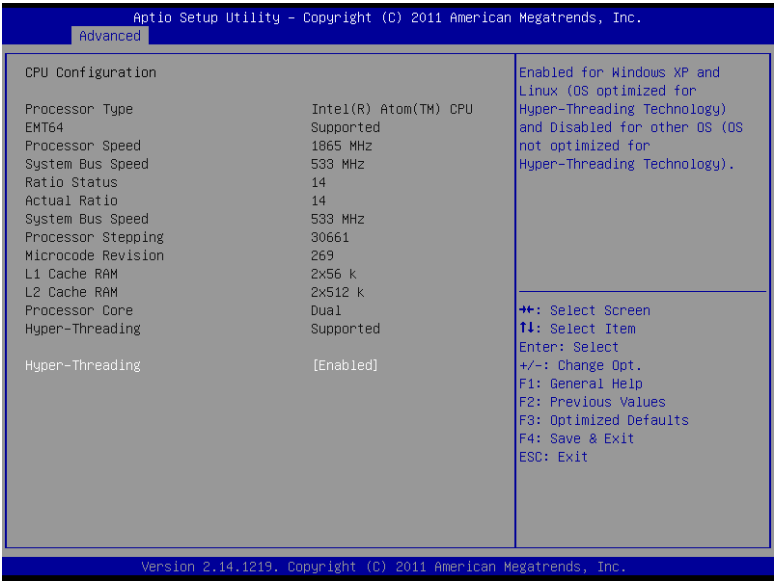
S5 RTC Wake Settings



Options summary:

Wake system with Fixed Time		Enable	
		Disable	Optimal Default, Failsafe Default
	Wake up hour	0	
	Wake up minute	0	
	Wake up second	0	
Wake system with Dynamic Time		Enable	
		Disable	Optimal Default, Failsafe Default
	Wake up minute increase	0	
Select RTC wake mode			

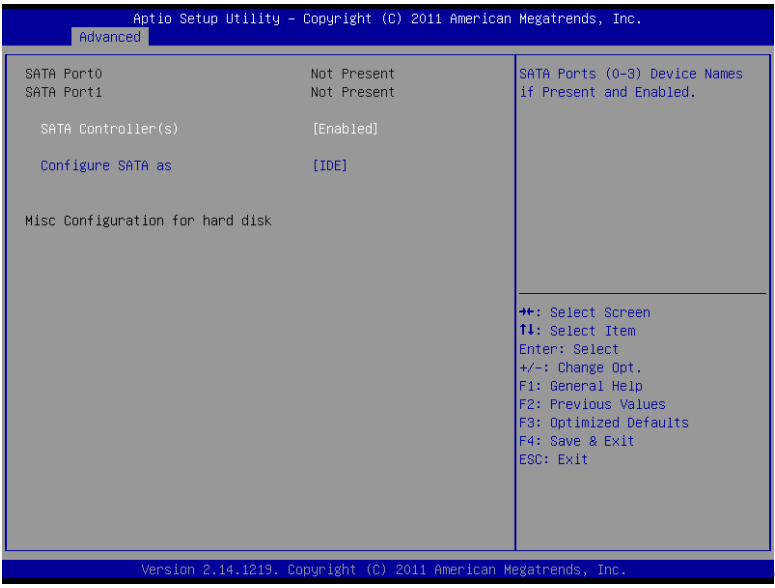
CPU Configuration



Options summary :

Hyper-Threading	Disabled	
	Enabled	Optimal Default, Failsafe Default
En/Disable CPU Hyper-Threading function		

SATA Configuration



O

Options summary :

SATA Mode	IDE	Default
	AHCI	
IDE: Configure SATA controllers as legacy IDE AHCI: Configure SATA controllers to operate in AHCI mode		
En/Disable SATA Port		

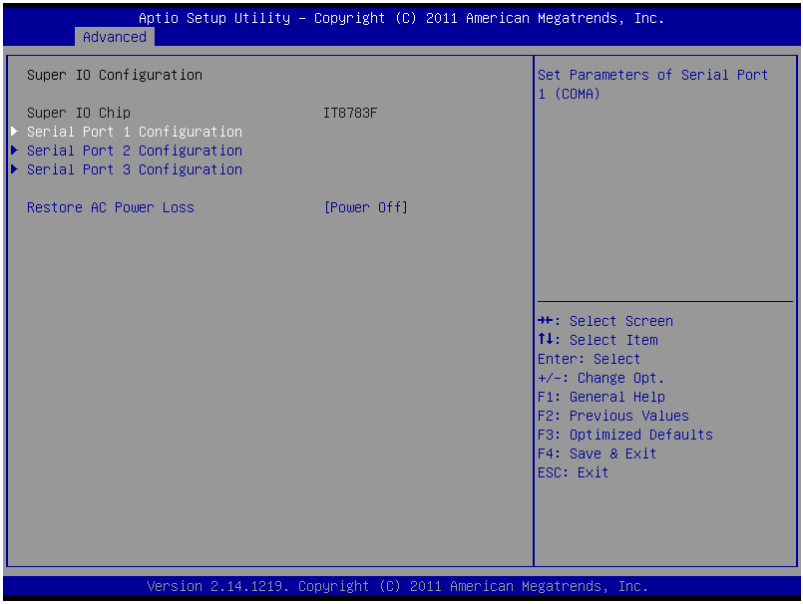
USB Configuration

Options summary:

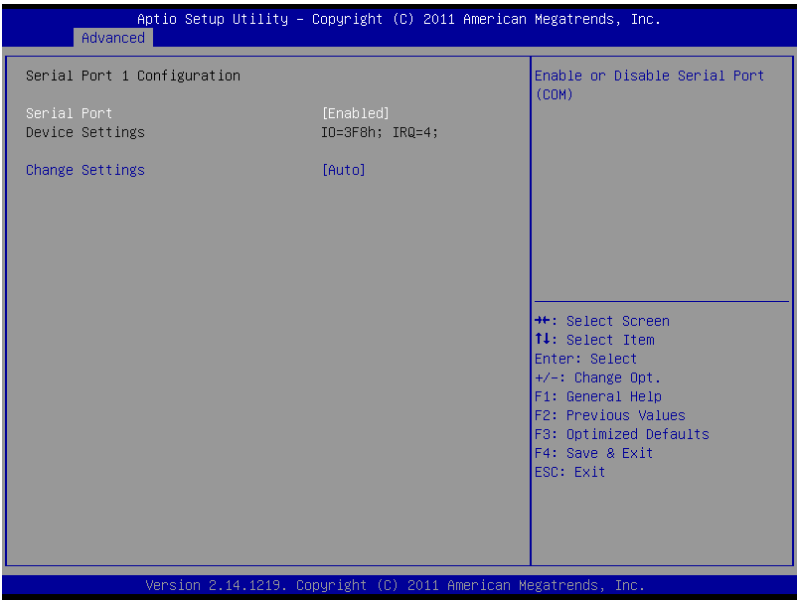
Legacy USB Support	Enabled	Optimal Default, Failsafe Default
	Disabled	
	Auto	
Enables BIOS Support for Legacy USB Support. When enabled, USB can be functional in legacy environment like DOS. AUTO option disables legacy support if no USB devices are connected		
Device Name (Emulation Type)	Auto	Optimal Default, Failsafe Default
	Floppy	
	Forced FDD	

	Hard Disk	
	CDROM	
If Auto. USB devices less than 530MB will be emulated as Floppy and remaining as Floppy and remaining as hard drive. Forced FDD option can be used to force a HDD formatted drive to boot as FDD(Ex. ZIP drive)		

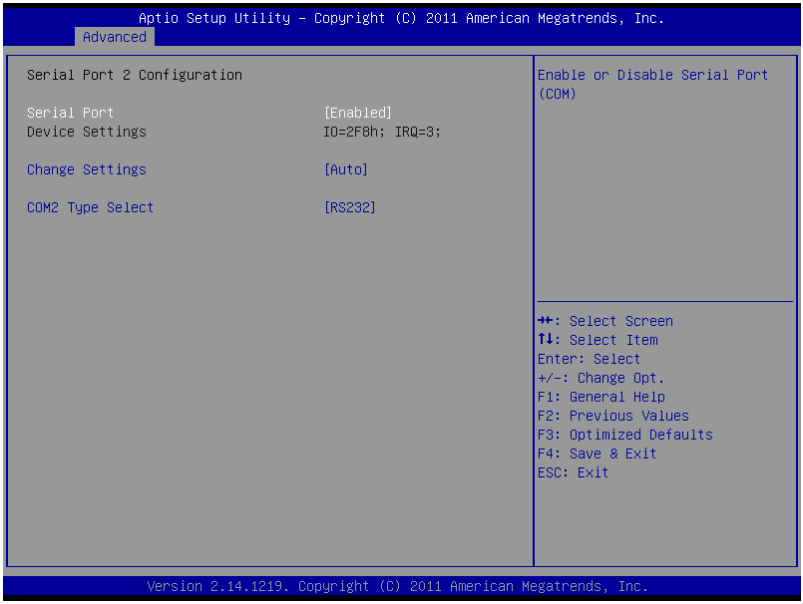
Super IO Configuration



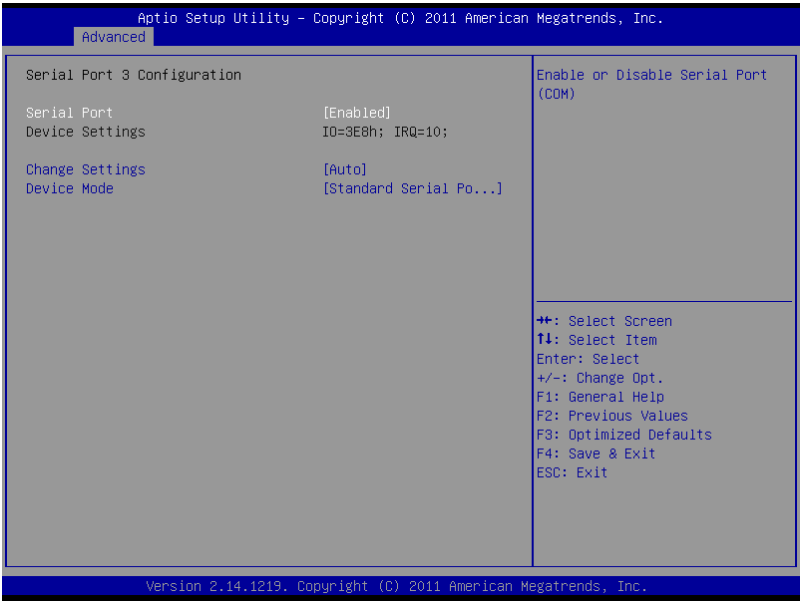
Serial Port 1 Configuration



Serial Port 2 Configuration



Serial Port 3 Configuration

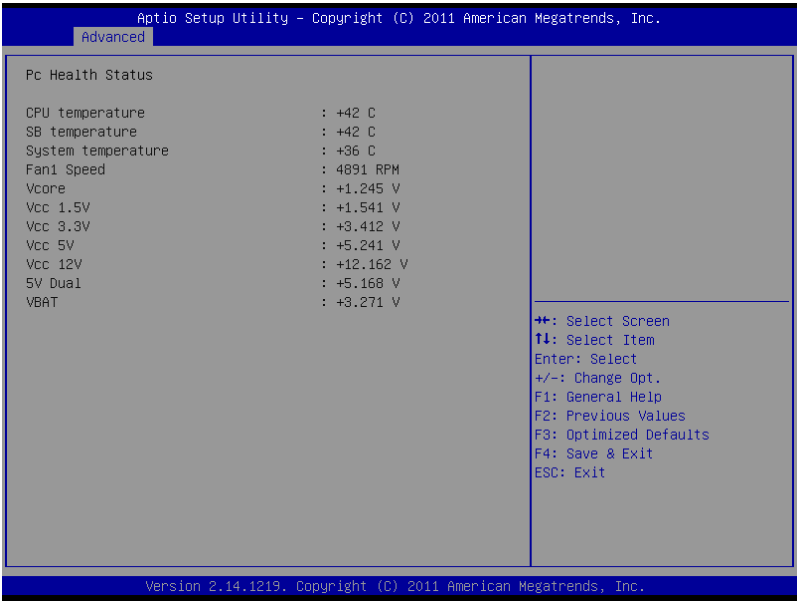


Options summary:

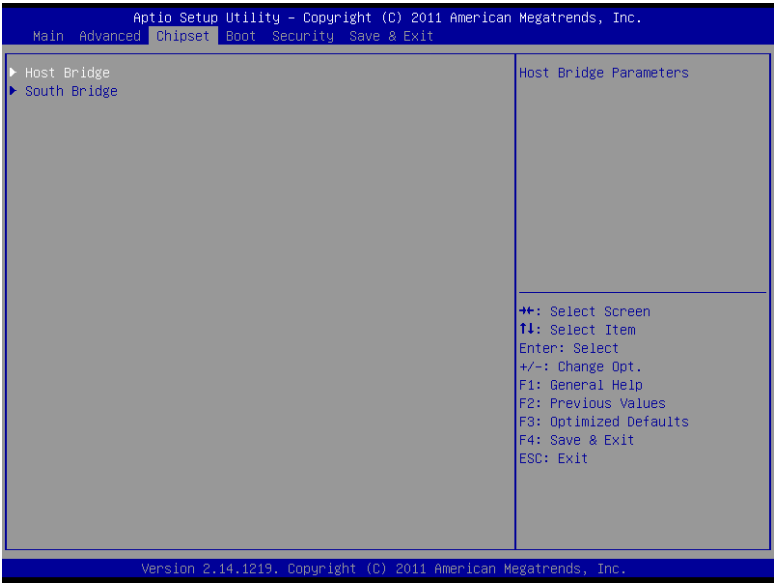
Serial Port	Disabled	
	Enabled	
Allows BIOS to En/Disable correspond serial port.		
Change Settings (Serial Port 1)	Auto	Default
	IO=3F8h; IRQ=4;	
	IO=3F8h; IRQ=3,4;	
	IO=2F8h; IRQ=3,4;	
	IO=3E8h; IRQ=10,11;	
	IO=2E8h; IRQ=10,11	

Allows BIOS to Select Serial Port resource.		
Change Settings (Serial Port 2)	Auto	Default
	IO=2F8h; IRQ=3;	
	IO=3F8h; IRQ=3,4;	
	IO=2F8h; IRQ=3,4;	
	IO=3E8h; IRQ=10,11;	
	IO=2E8h; IRQ=10,11	
COM2 Type Select	RS232	Default
	RS422	
	RS485	
Allows BIOS to Select Serial Port resource.		
Change Settings (Serial Port 3)	Auto	Default
	IO=3E8h; IRQ=11;	
	IO=3E8h; IRQ=10,11;	
	IO=2E8h; IRQ=10,11;	
	IO=3E8h; IRQ=10,11;	
	IO=2E8h; IRQ=10,11	
Device Mode	Standard Serial Port Mode	Default
	IrDA 1.0 (HP SIR) Mode	
	ASKIR Mode	
Restore on AC Power Loss	Power Off	Default
	Power On	
	Last State	
Select the action system to take when restoring from power loss.		

H/W Monitor


Aptio Setup Utility – Copyright (C) 2011 American Megatrends, Inc.
Advanced
Pc Health Status
CPU temperature : +42 C
SB temperature : +42 C
System temperature : +36 C
Fan1 Speed : 4891 RPM
Vcore : +1.245 V
Vcc 1.5V : +1.541 V
Vcc 3.3V : +3.412 V
Vcc 5V : +5.241 V
Vcc 12V : +12.162 V
5V Dual : +5.168 V
VBAT : +3.271 V
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.

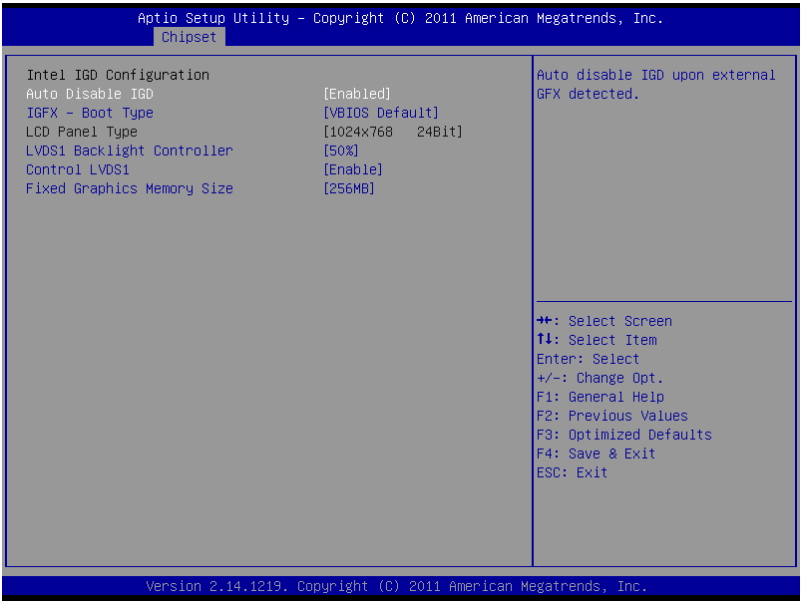
Chipset


Aptio Setup Utility – Copyright (C) 2011 American Megatrends, Inc.
Main Advanced Chipset Boot Security Save & Exit
Host Bridge
South Bridge
Host Bridge Parameters
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.

Host Bridge


Aptio Setup Utility – Copyright (C) 2011 American Megatrends, Inc.
Chipset
Intel IGD Configuration
******* Memory Information *******
Memory Frequency 1067 MHz(DDR3)
Total Memory 2048 MB
DIMM#0 Not Present
DIMM#1 2048 MB
Config Intel IGD Settings.
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.

Graphics Configuration

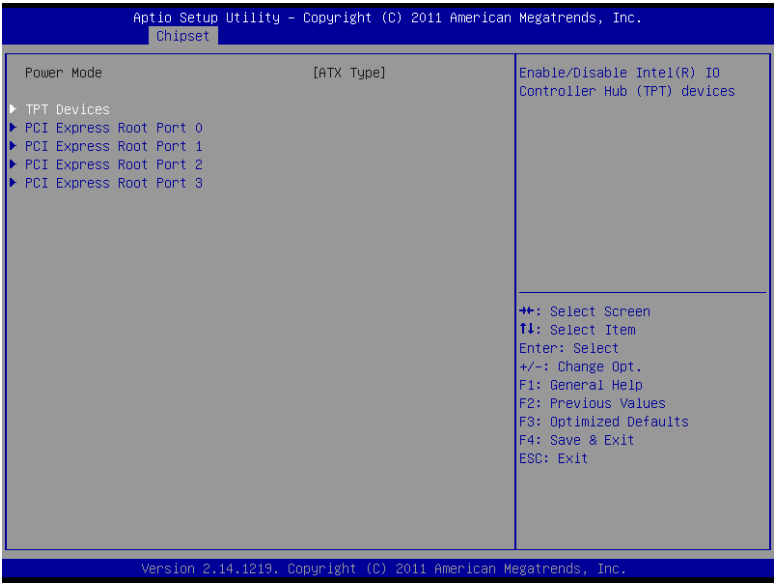


Options summary :

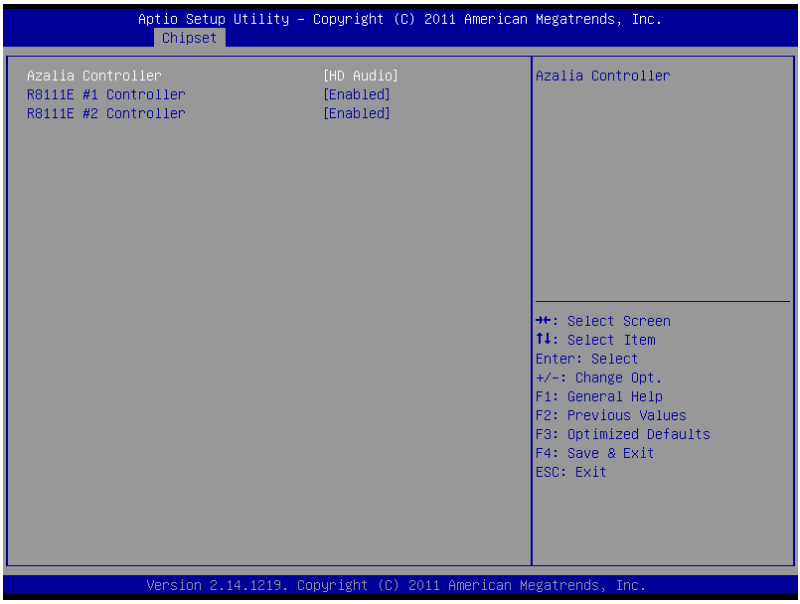
Auto Disable IGE	Disable	
	Enable	Default
Auto disable IGE upon external GFX detected		
IGFX – Boot Type	VBIOS Default	Default
	CRT	
	1st LVDS	
	CRT+1st LVDS	
Select boot display device VBIOS Default – Display automatically according to VBIOS algorithm		

LCD Panel Type	1024x768 24Bit	Default
Select LCD panel used by internal Graphics Device by selecting the appropriate setup item.		
LVDS1 Backlight Controller	100%	Default
	75%	
	50%	
	25%	
	0%	
Adjust backlight brightness		
Control LVDS1	Disable	Default
	Enable	
Select LFP source		
Fixed Graphics Memory Size	128MB	Default
	256MB	

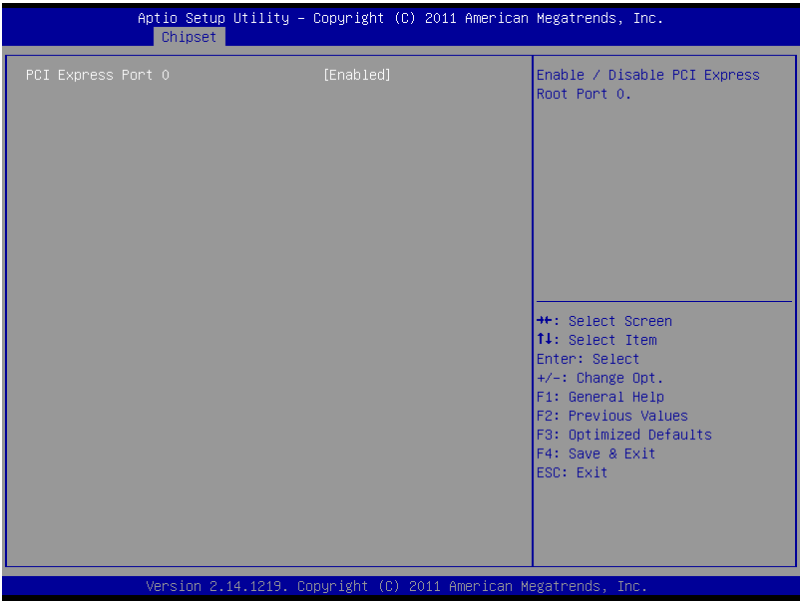
South Bridge



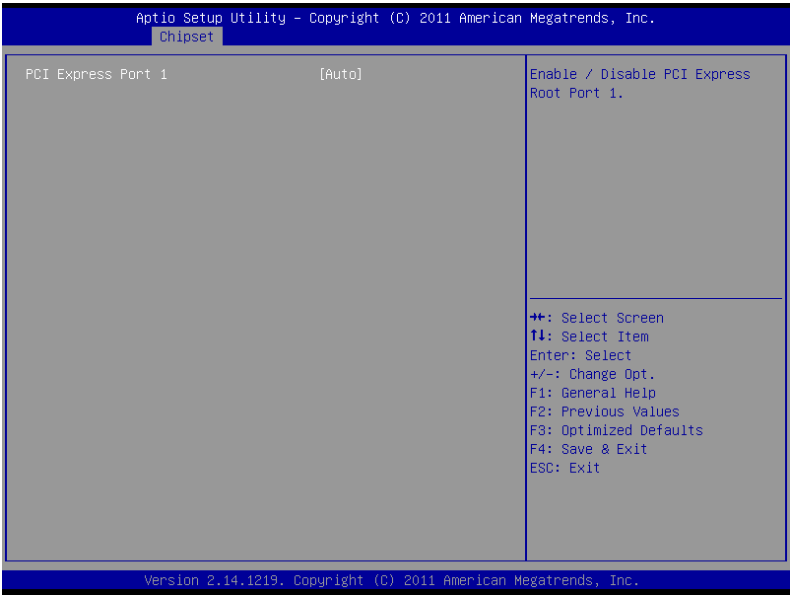
TPT Device


Aptio Setup Utility – Copyright (C) 2011 American Megatrends, Inc.
Chipset
Azalia Controller [HD Audio]
R8111E #1 Controller [Enabled]
R8111E #2 Controller [Enabled]
Azalia Controller
→←: Select Screen
↑↓: Select Item
Enter: Select
+/-: Change Opt.
F1: General Help
F2: Previous Values
F3: Optimized Defaults
F4: Save & Exit
ESC: Exit
Version 2.14.1219. Copyright (C) 2011 American Megatrends, Inc.

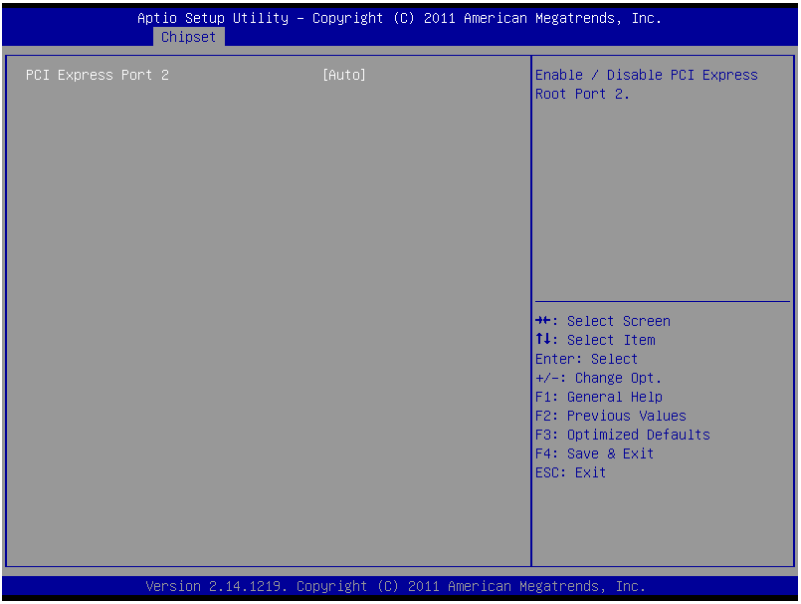
PCI Express Root Port 0



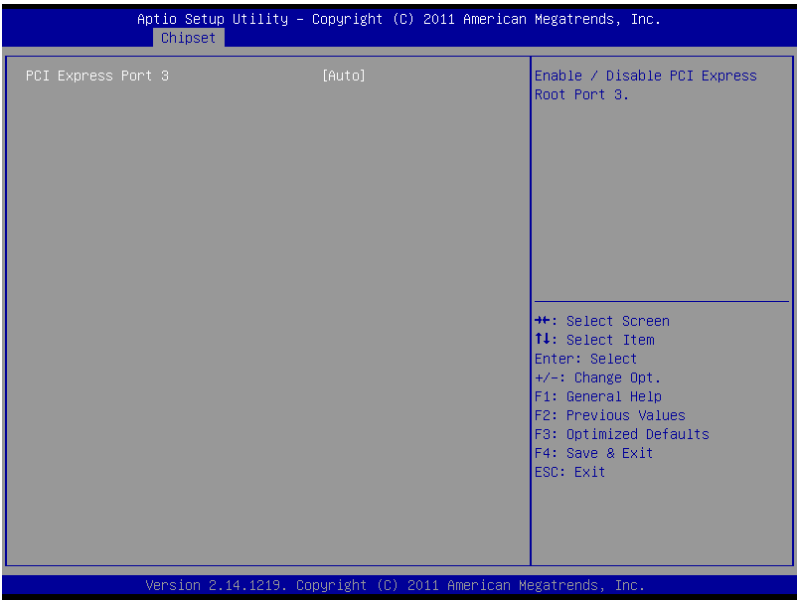
PCI Express Root Port 1



PCI Express Root Port 2



PCI Express Root Port 3

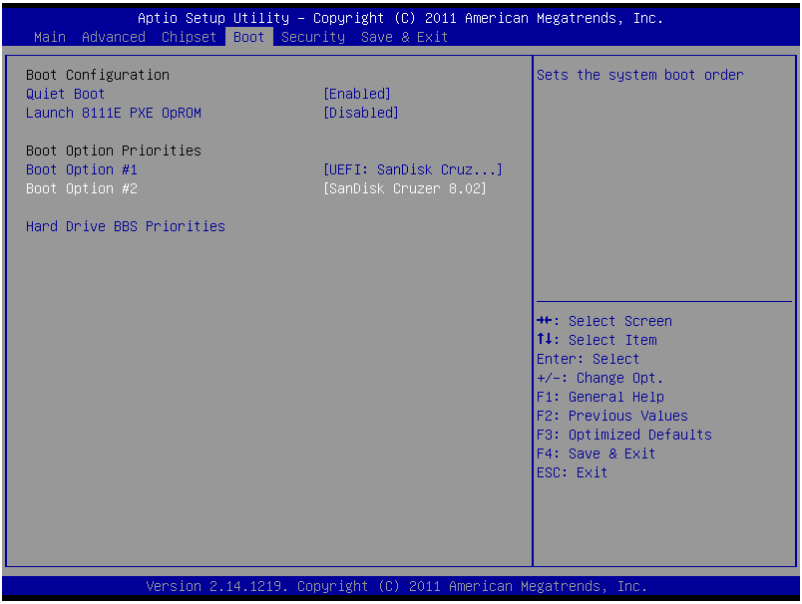


Options summary :

Power Mode	ATX Type	Optimal Default, Failsafe Default
	AT Type	
Select Power Mode: ATX Type: Normal ACPI support AT Type: Suspend/Sleep disabled, and Always On when restoring from power failure.		
Azalia HD Audio	Disabled	
	HD Audio	Optimal Default, Failsafe Default
Enabling/Disabling HD Audio controller.		

R8111 #x Controller	Disabled	
	Enabled	Optimal Default, Failsafe Default
Enabling/Disabling 8111E controller		
PCI Express Root Port 0	Disabled	
	Enabled	Optimal Default, Failsafe Default
Enabling/Disabling PCI Express root ports		
PCI Express Root Port x	Disabled	
	Enabled	
	Auto	Optimal Default, Failsafe Default
Enabling/Disabling PCI Express root ports		

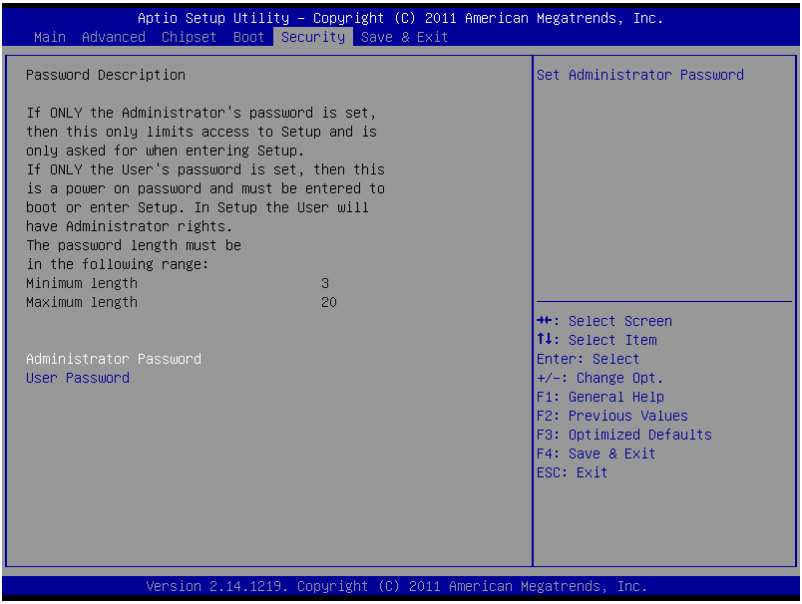
Setup submenu: Boot



Options summary :

Quiet Boot	Disabled	
	Enabled	Default
En/Disable showing boot logo.		
Launch 8111E PXE OpROM	Disabled	Default
	Enabled	
En/Disable PXE boot for 8111E LAN		

Setup submenu: Security



Change User/Supervisor Password

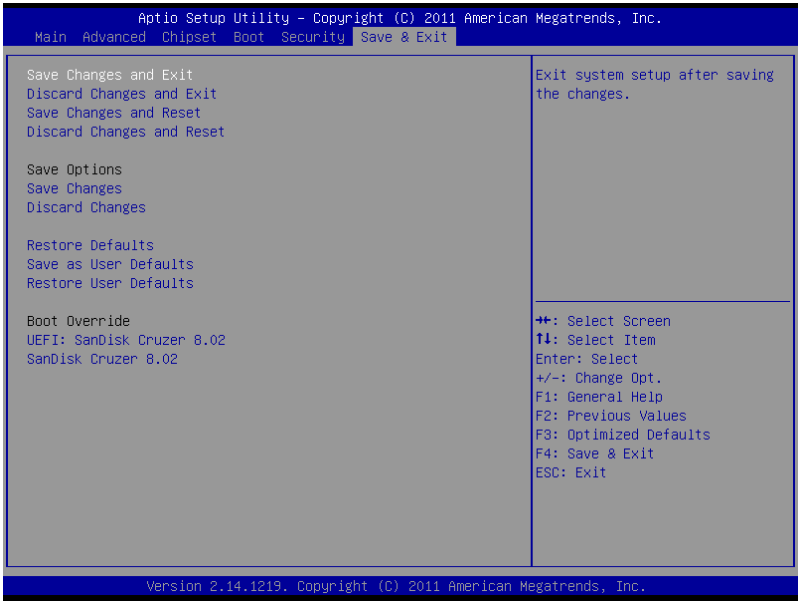
You can install a Supervisor password, and if you install a supervisor password, you can then install a user password. A user password does not provide access to many of the features in the Setup utility.

If you highlight these items and press Enter, a dialog box appears which lets you enter a password. You can enter no more than six letters or numbers. Press Enter after you have typed in the password. A second dialog box asks you to retype the password for confirmation. Press Enter after you have retyped it correctly. The password is required at boot time, or when the user enters the Setup utility.

Removing the Password

Highlight this item and type in the current password. At the next dialog box press Enter to disable password protection.

Setup submenu: Exit



Chapter

4

Driver Installation

The ACP-2153 comes with a CD-ROM that contains all drivers and utilities that meet your needs.

Follow the sequence below to install the drivers:

Step 1 – Install Chipset Driver

Step 2 – Install VGA Driver

Step 3 – Install LAN Driver

Step 4 – Install Audio Driver

Step 5 – Install AHCI Driver

Step 6 – Install Touch Panel Driver

Please read instructions below for further detailed installations.

4.1 Installation:

Insert the ACP-2153 CD-ROM into the CD-ROM Drive. And install the drivers from Step 1 to Step 6 in order.

Step 1 – Install Chipset Driver

1. Click on the ***STEP1-CHIPSET*** and double click on the ***Setup.exe*** file
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 2 – Install VGA Driver

1. Click on the ***STEP2-VGA*** folder and double click on the ***Setup.exe*** file
2. Follow the instructions that the window shows
3. The system will help you install the driver automatically

Step 3 – Install LAN Driver

1. Click on the ***STEP3-LAN*** folder and select the OS folder your system is
2. Double click on the ***setup.exe*** located in each OS folder
3. Follow the instructions that the window shows
4. The system will help you install the driver automatically

Step 4 – Install Audio Driver

1. Click on the ***STEP4-AUDIO*** folder and select the OS folder your system is

2. Double click on the ***Setup.exe*** located in each OS folder
3. Follow the instructions that the window shows
4. The system will help you install the driver automatically

Step 5 – Install AHCI Driver

1. Click on the ***STEP5-AHCI*** folder and select the ***WIN7_32*** folder
2. Double click on the ***setup.exe*** file
3. Follow the instructions that the window shows
4. The system will help you install the driver automatically

Step 6 – Install Touch Panel Driver

1. Click on the ***STEP6-TOUCH*** folder and select the ***XP*** folder
2. Double click on the ***ModifyDBArea.exe*** located in the ***XP*** folder
3. Follow the instructions that the window shows
4. The system will help you install the driver automatically

Appendix

A

Programming the Watchdog Timer

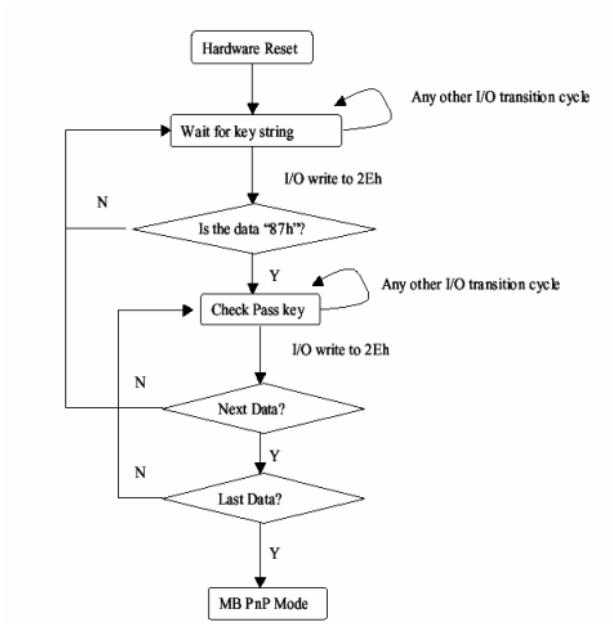
A.1 Programming

ACP-2153 utilizes ITE 8783 chipset as its watchdog timer controller. Below are the procedures to complete its configuration and the AAEON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

Configuring Sequence Description

After the hardware reset or power-on reset, the ITE 8783 enters the

normal mode with all logical devices disabled except KBC. The initial state (enable bit) of this logical device (KBC) is determined by the state of pin 121 (DTR1#) at the falling edge of the system reset during power-on reset.



There are three steps to complete the configuration setup: (1) Enter the MB PnP Mode; (2) Modify the data of configuration registers; (3) Exit the MB PnP Mode. Undesired result may occur if the MB PnP Mode is not exited normally.

(1) Enter the MB PnP Mode

To enter the MB PnP Mode, four special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform four write opera-tions to the Special Address port (2EH). Two different enter keys are provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
87h, 01h, 55h, 55h:	2Eh	2Fh

(2) Modify the Data of the Registers

All configuration registers can be accessed after entering the MB PnP Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

(3) Exit the MB PnP Mode

Set bit 1 of the configure control register (Index=02h) to 1 to exit the MB PnP Mode.

WatchDog Timer Configuration Registers

LDN	Index	R/W	Reset	Configuration Register or Action
All	02h	W	NA	Configure Control

07h	71h	R/W	00h	Watch Dog Timer Control Register
07h	72h	R/W	001s0000b	Watch Dog Timer Configuration Register
07h	73h	R/W	38h	Watch Dog Timer Time-out Value (LSB) Register
07h	74h	R/W	00h	Watch Dog Timer Time-out Value (MSB) Register

Configure Control (Index=02h)

This register is write only. Its values are not sticky; that is to say, a hardware reset will automatically clear the bits, and does not require the software to clear them.

Bit	Description
7-2	**Reserved**
1	Returns to the "Wait for Key" state. This bit is used when the configuration sequence is completed.
0	Resets all logical devices and restores configuration registers to their power-on states.

Watch Dog Timer 1, 2, 3 Control Register (Index=71h,81h,91h Default=00h)

Bit	Description
7	**WDT Timeout Enable(WTE)** 1: Disable. 0: Enable.
6	**WDT Reset upon Mouse Interrupt(WRKMI)** 0: Disable. 1: Enable.
5	**WDT Reset upon Keyboard Interrupt(WRKBI)** 0: Disable. 1: Enable.
4	**Reserved**
3-2	**Reserved**
1	**Force Time-out(FTO)** This bit is self-clearing.
0	**WDT Status(WS)** 1: WDT value reaches 0. 0: WDT value is not 0.

Watch Dog Timer 1, 2, 3 Configuration Register (Index=72h, 82h, 92h Default=001s0000b)

Bit	Description
7	**WDT Time-out Value Select 1 (WTVS)** 1: Second 0: Minute
6	**WDT Output through KRST (Pulse) Enable(WOKE)** 1: Enable 0: Disable
5	**WDT Time-out value Extra select(WTVES)** 1: 64ms x WDT Timer-out value (default = 4s) 0: Determined by WDT Time-out value select 1 (bit 7 of this register)
4	**WDT Output through PWROK (Pulse) Enable(WOPE)** 1: Enable 0: Disable During LRESET#, this bit is selected by JP7 power-on strapping option
3-0	**Select interrupt level [Note1] for WDT(SIL)**

Watch Dog Timer 1,2,3 Time-Out Value (LSB) Register (Index=73h,83h,93h, Default=38h)

Bit	Description
7-0	**WDT Time-out Value 7-0(WTV)**

Watch Dog Timer 1,2,3 Time-Out Value (MSB) Register (Index=74h,84h,94h Default=00h)

Bit	Description
7-0	**WDT Time-out Value 15-8(WTV)**

A.2 ITE8783 Watchdog Timer Initial Program

```
.MODEL SMALL
.CODE
Main:
CALL Enter_Configuration_mode
CALL Check_Chip
mov cl, 7
call Set_Logic_Device
;time setting
mov cl, 10 ; 10 Sec
dec al
Watch_Dog_Setting:
;Timer setting
mov al, cl
mov cl, 73h
call Superio_Set_Reg
;Clear by keyboard or mouse interrupt
mov al, 0f0h
mov cl, 71h
call Superio_Set_Reg
;unit is second.
mov al, 0C0H
mov cl, 72h
```

```
call Superio_Set_Reg
; game port enable
mov cl, 9
call Set_Logic_Device

Initial_OK:
CALL Exit_Configuration_mode
MOV AH,4Ch
INT 21h

Enter_Configuration_Mode PROC NEAR
MOV SI,WORD PTR CS:[Offset Cfg_Port]

MOV DX,02Eh
MOV CX,04h
Init_1:
MOV AL,BYTE PTR CS:[SI]
OUT DX,AL
INC SI
LOOP Init_1
RET
Enter_Configuration_Mode ENDP

Exit_Configuration_Mode PROC NEAR
MOV AX,0202h
```

```
CALL Write_Configuration_Data
RET
Exit_Configuration_Mode ENDP

Check_Chip PROC NEAR

MOV AL,20h
CALL Read_Configuration_Data
CMP AL,87h
JNE Not_Initial

MOV AL,21h
CALL Read_Configuration_Data
CMP AL,81h
JNE Not_Initial

Need_Initial:
STC
RET
Not_Initial:
CLC
RET
Check_Chip ENDP
Read_Configuration_Data PROC NEAR
MOV DX,WORD PTR CS:[Cfg_Port+04h]
```

```
OUT DX,AL
MOV DX,WORD PTR CS:[Cfg_Port+06h]
IN AL,DX
RET
Read_Configuration_Data ENDP

Write_Configuration_Data PROC NEAR
MOV DX,WORD PTR CS:[Cfg_Port+04h]
OUT DX,AL
XCHG AL,AH
MOV DX,WORD PTR CS:[Cfg_Port+06h]
OUT DX,AL
RET
Write_Configuration_Data ENDP

Superio_Set_Reg proc near
push ax
MOV DX,WORD PTR CS:[Cfg_Port+04h]
mov al,cl
out dx,al
pop ax
inc dx
out dx,al
ret
Superio_Set_Reg endp.Set_Logic_Device proc near
```

```
Set_Logic_Device      proc      near
push ax
push cx
xchg al,cl
mov cl,07h
call Superio_Set_Reg
pop cx
pop ax
ret
Set_Logic_Device endp

;Select 02Eh->Index Port, 02Fh->Data Port
Cfg_Port DB 087h,001h,055h,055h
DW 02Eh,02Fh
```

END Main

Note: Interrupt level mapping

0Fh-Dh: not valid

0Ch: IRQ12

.

.

03h: IRQ3

02h: not valid

01h: IRQ1

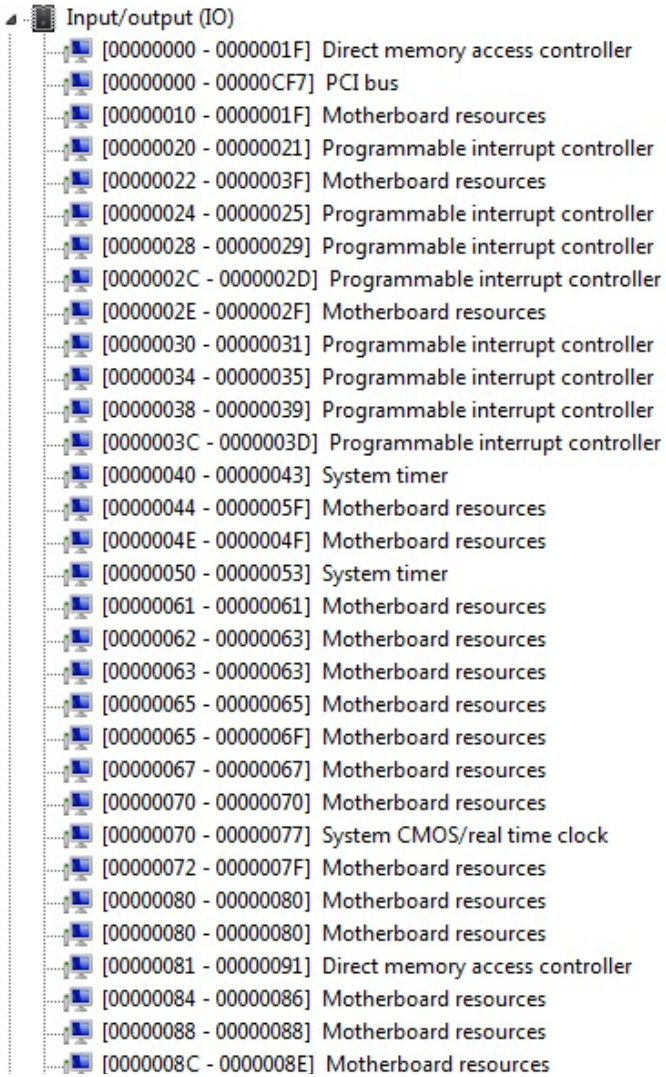
00h: no interrupt selected

Appendix

B

I/O Information

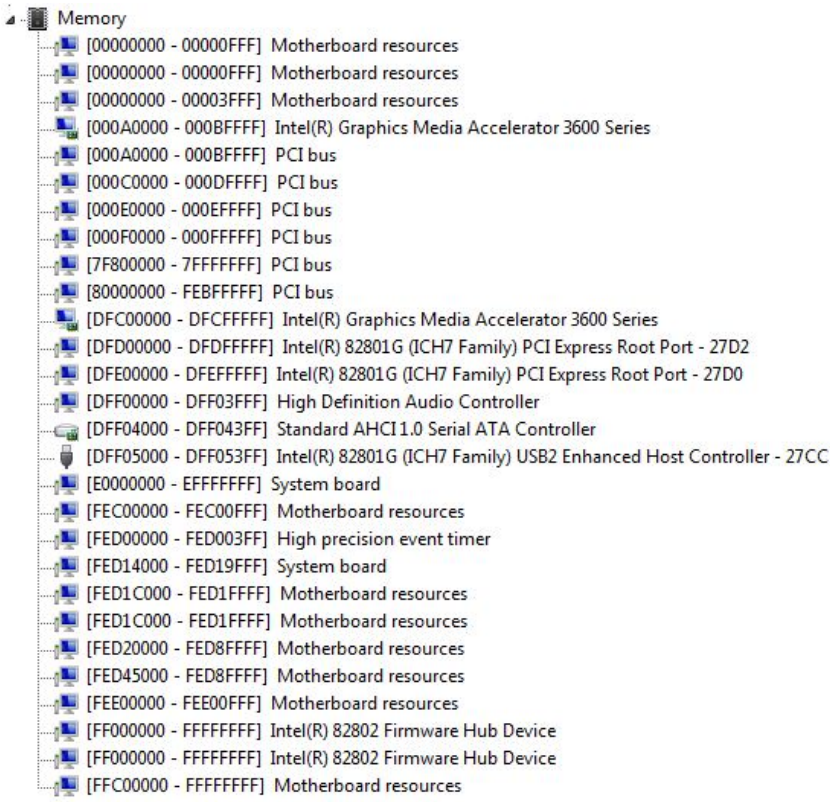
B.1 I/O Address Map



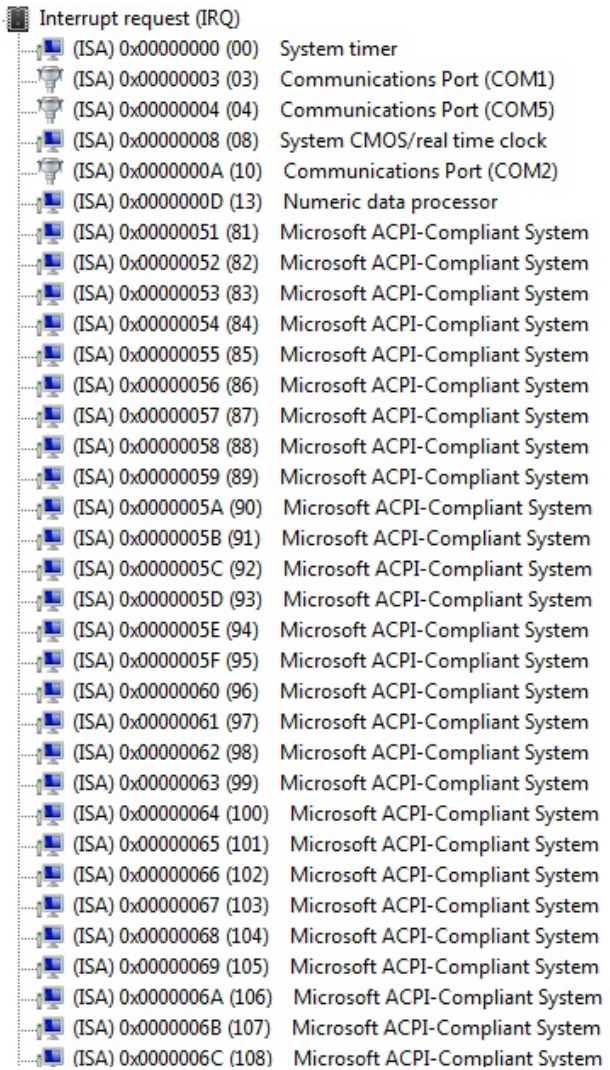
[00000090 - 0000009F] Motherboard resources
[00000092 - 00000092] Motherboard resources
[00000093 - 0000009F] Direct memory access controller
[000000A0 - 000000A1] Programmable interrupt controller
[000000A2 - 000000BF] Motherboard resources
[000000A4 - 000000A5] Programmable interrupt controller
[000000A8 - 000000A9] Programmable interrupt controller
[000000AC - 000000AD] Programmable interrupt controller
[000000B0 - 000000B1] Programmable interrupt controller
[000000B2 - 000000B3] Motherboard resources
[000000B4 - 000000B5] Programmable interrupt controller
[000000B8 - 000000B9] Programmable interrupt controller
[000000BC - 000000BD] Programmable interrupt controller
[000000C0 - 000000DF] Direct memory access controller
[000000E0 - 000000EF] Motherboard resources
[000000F0 - 000000F0] Numeric data processor
[000002F8 - 000002FF] Communications Port (COM1)
[000003B0 - 000003BB] Intel(R) Graphics Media Accelerator 3600 Series
[000003C0 - 000003DF] Intel(R) Graphics Media Accelerator 3600 Series
[000003E8 - 000003EF] Communications Port (COM2)
[000003F8 - 000003FF] Communications Port (COM5)
[00000400 - 0000047F] Motherboard resources
[00000400 - 0000047F] Motherboard resources
[000004D0 - 000004D1] Motherboard resources
[000004D0 - 000004D1] Programmable interrupt controller
[00000500 - 0000053F] Motherboard resources
[00000500 - 0000057F] Motherboard resources
[00000600 - 0000061F] Motherboard resources
[00000680 - 0000069F] Motherboard resources
[000006A0 - 000006AF] Motherboard resources
[000006B0 - 000006EF] Motherboard resources
[00000A00 - 00000A1F] Motherboard resources
[00000A20 - 00000A2F] Motherboard resources
[00000A30 - 00000A3F] Motherboard resources
[00000D00 - 0000FFFF] PCI bus

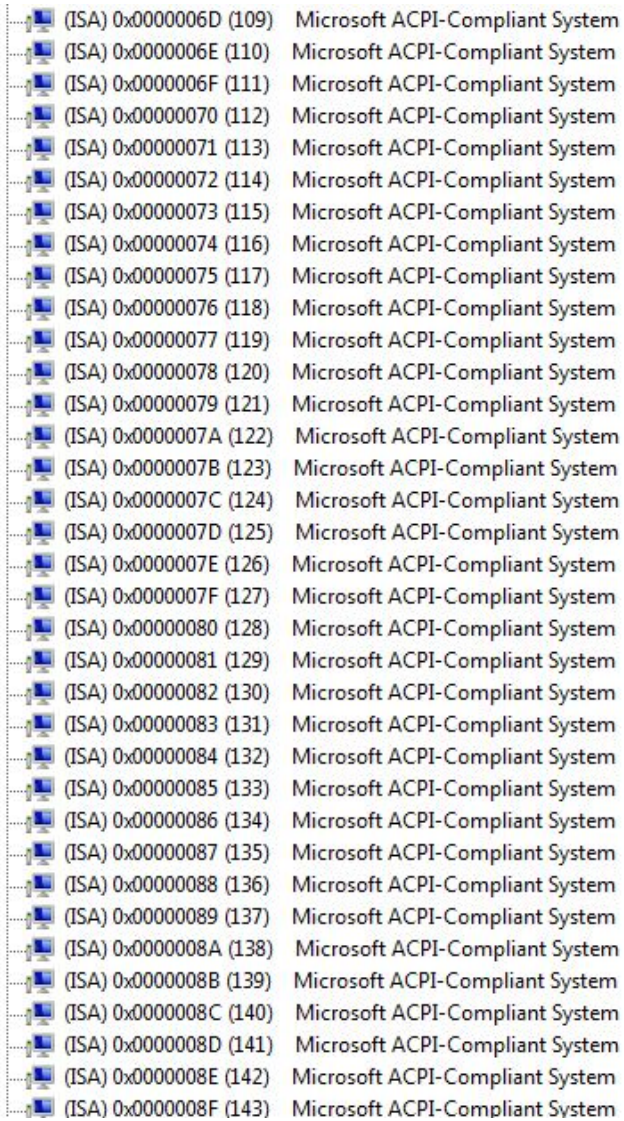
[00001000 - 0000100F] Motherboard resources
[0000D000 - 0000DFFF] Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D2
[0000E000 - 0000EFFF] Intel(R) 82801G (ICH7 Family) PCI Express Root Port - 27D0
[0000F000 - 0000F01F] Intel(R) 82801G (ICH7 Family) SMBus Controller - 27DA
[0000F020 - 0000F02F] Standard AHCI 1.0 Serial ATA Controller
[0000F040 - 0000F05F] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CB
[0000F060 - 0000F07F] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27CA
[0000F080 - 0000F09F] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C9
[0000F0A0 - 0000F0BF] Intel(R) 82801G (ICH7 Family) USB Universal Host Controller - 27C8
[0000F0C0 - 0000F0C3] Standard AHCI 1.0 Serial ATA Controller
[0000F0D0 - 0000F0D7] Standard AHCI 1.0 Serial ATA Controller
[0000F0E0 - 0000F0E3] Standard AHCI 1.0 Serial ATA Controller
[0000F0F0 - 0000F0F7] Standard AHCI 1.0 Serial ATA Controller
[0000F100 - 0000F107] Intel(R) Graphics Media Accelerator 3600 Series
[0000FFFF - 0000FFFF] Motherboard resources
[0000FFFF - 0000FFFF] Motherboard resources

B.2 1st MB Memory Address Map

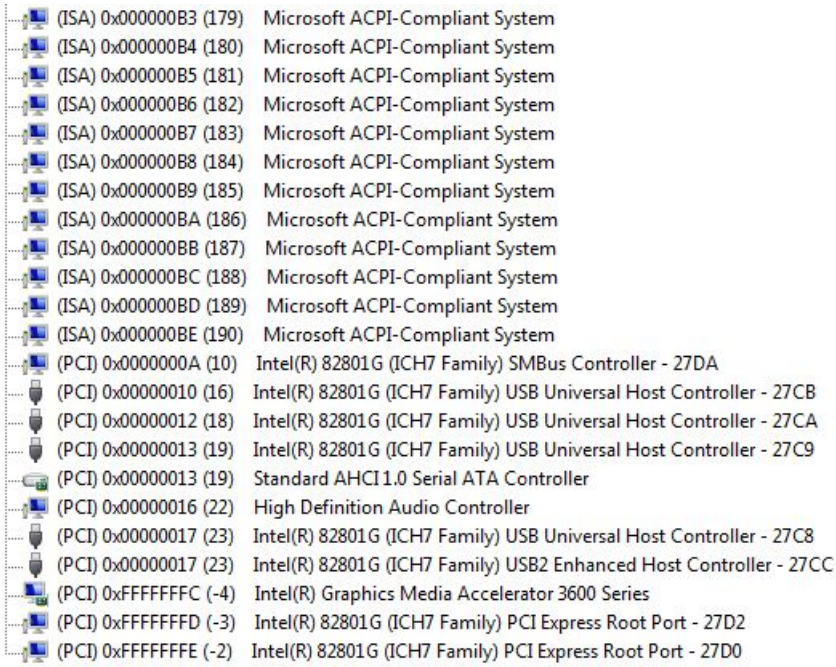


B.3 IRQ Mapping Chart




(ISA) 0x0000006D (109) Microsoft ACPI-Compliant System
(ISA) 0x0000006E (110) Microsoft ACPI-Compliant System
(ISA) 0x0000006F (111) Microsoft ACPI-Compliant System
(ISA) 0x00000070 (112) Microsoft ACPI-Compliant System
(ISA) 0x00000071 (113) Microsoft ACPI-Compliant System
(ISA) 0x00000072 (114) Microsoft ACPI-Compliant System
(ISA) 0x00000073 (115) Microsoft ACPI-Compliant System
(ISA) 0x00000074 (116) Microsoft ACPI-Compliant System
(ISA) 0x00000075 (117) Microsoft ACPI-Compliant System
(ISA) 0x00000076 (118) Microsoft ACPI-Compliant System
(ISA) 0x00000077 (119) Microsoft ACPI-Compliant System
(ISA) 0x00000078 (120) Microsoft ACPI-Compliant System
(ISA) 0x00000079 (121) Microsoft ACPI-Compliant System
(ISA) 0x0000007A (122) Microsoft ACPI-Compliant System
(ISA) 0x0000007B (123) Microsoft ACPI-Compliant System
(ISA) 0x0000007C (124) Microsoft ACPI-Compliant System
(ISA) 0x0000007D (125) Microsoft ACPI-Compliant System
(ISA) 0x0000007E (126) Microsoft ACPI-Compliant System
(ISA) 0x0000007F (127) Microsoft ACPI-Compliant System
(ISA) 0x00000080 (128) Microsoft ACPI-Compliant System
(ISA) 0x00000081 (129) Microsoft ACPI-Compliant System
(ISA) 0x00000082 (130) Microsoft ACPI-Compliant System
(ISA) 0x00000083 (131) Microsoft ACPI-Compliant System
(ISA) 0x00000084 (132) Microsoft ACPI-Compliant System
(ISA) 0x00000085 (133) Microsoft ACPI-Compliant System
(ISA) 0x00000086 (134) Microsoft ACPI-Compliant System
(ISA) 0x00000087 (135) Microsoft ACPI-Compliant System
(ISA) 0x00000088 (136) Microsoft ACPI-Compliant System
(ISA) 0x00000089 (137) Microsoft ACPI-Compliant System
(ISA) 0x0000008A (138) Microsoft ACPI-Compliant System
(ISA) 0x0000008B (139) Microsoft ACPI-Compliant System
(ISA) 0x0000008C (140) Microsoft ACPI-Compliant System
(ISA) 0x0000008D (141) Microsoft ACPI-Compliant System
(ISA) 0x0000008E (142) Microsoft ACPI-Compliant System
(ISA) 0x0000008F (143) Microsoft ACPI-Compliant System

(ISA) 0x00000090 (144) Microsoft ACPI-Compliant System
(ISA) 0x00000091 (145) Microsoft ACPI-Compliant System
(ISA) 0x00000092 (146) Microsoft ACPI-Compliant System
(ISA) 0x00000093 (147) Microsoft ACPI-Compliant System
(ISA) 0x00000094 (148) Microsoft ACPI-Compliant System
(ISA) 0x00000095 (149) Microsoft ACPI-Compliant System
(ISA) 0x00000096 (150) Microsoft ACPI-Compliant System
(ISA) 0x00000097 (151) Microsoft ACPI-Compliant System
(ISA) 0x00000098 (152) Microsoft ACPI-Compliant System
(ISA) 0x00000099 (153) Microsoft ACPI-Compliant System
(ISA) 0x0000009A (154) Microsoft ACPI-Compliant System
(ISA) 0x0000009B (155) Microsoft ACPI-Compliant System
(ISA) 0x0000009C (156) Microsoft ACPI-Compliant System
(ISA) 0x0000009D (157) Microsoft ACPI-Compliant System
(ISA) 0x0000009E (158) Microsoft ACPI-Compliant System
(ISA) 0x0000009F (159) Microsoft ACPI-Compliant System
(ISA) 0x000000A0 (160) Microsoft ACPI-Compliant System
(ISA) 0x000000A1 (161) Microsoft ACPI-Compliant System
(ISA) 0x000000A2 (162) Microsoft ACPI-Compliant System
(ISA) 0x000000A3 (163) Microsoft ACPI-Compliant System
(ISA) 0x000000A4 (164) Microsoft ACPI-Compliant System
(ISA) 0x000000A5 (165) Microsoft ACPI-Compliant System
(ISA) 0x000000A6 (166) Microsoft ACPI-Compliant System
(ISA) 0x000000A7 (167) Microsoft ACPI-Compliant System
(ISA) 0x000000A8 (168) Microsoft ACPI-Compliant System
(ISA) 0x000000A9 (169) Microsoft ACPI-Compliant System
(ISA) 0x000000AA (170) Microsoft ACPI-Compliant System
(ISA) 0x000000AB (171) Microsoft ACPI-Compliant System
(ISA) 0x000000AC (172) Microsoft ACPI-Compliant System
(ISA) 0x000000AD (173) Microsoft ACPI-Compliant System
(ISA) 0x000000AE (174) Microsoft ACPI-Compliant System
(ISA) 0x000000AF (175) Microsoft ACPI-Compliant System
(ISA) 0x000000B0 (176) Microsoft ACPI-Compliant System
(ISA) 0x000000B1 (177) Microsoft ACPI-Compliant System
(ISA) 0x000000B2 (178) Microsoft ACPI-Compliant System



B.4 DMA Channel Assignments